

AMERICAN UNIVERSITY OF BEIRUT

A METHOD FOR VERIFYING
CHOREOGRAPHIES AND THEIR
IMPLEMENTATIONS

by

AL-ABBASS ADHAM KHALIL

A thesis

submitted in partial fulfillment of the requirements
for the degree of Master of Science
to the Department of Computer Science
of the Faculty of Arts and Sciences
at the American University of Beirut

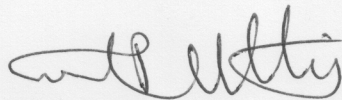
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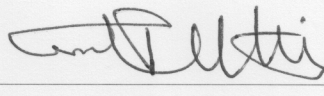
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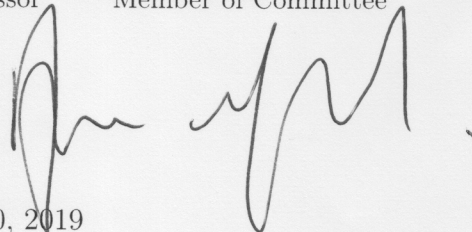
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An Abstract of the Thesis of

Al-Abbass Adham Khalil for Masters of Science
Major: Computer Science

Title: A Method for Verifying Choreographies and Their Implementations

A global choreography defines a communication pattern over a set of ports. The ports are partitioned into subsets, each subset being the ports that belong to a given process. From a choreography and an interaction architecture, a distributed implementation can be generated automatically. The implementation can then be analyzed for correctness using standard methods such as model checking, but this is subject to state-explosion. A more efficient approach is to verify that the choreography is correct, and to establish that the implementation automatically inherits the correctness properties of the choreography. Because the choreography is centralized, analyzing it provides a more manageable abstract view and it incurs less state explosion. We present such an approach in this thesis, along with several case studies illustrating its advantages in practice.

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Chapter 1

Introduction

1.1 Background

Proving the correctness of a hardware or software system is an integral step in achieving its reliability. Model checking is the process where given a model of a system, check if this system satisfies a given property. Some of the properties can be safety requirements such as absence of deadlock, satisfaction of integrity constraints, and the absence (non-reachability) of bad states that can cause the system to crash or malfunction.

As the system model becomes more complex, more states and variables are involved and thus we reach “state explosion”, where the size of the system state space has grown exponentially with the number of processes and state variables, and the time it takes to model check the system becomes unfeasible. One of the methods to reduce this complexity is to use *abstraction*, where first we simplify the system before proving its properties. However, the simplified system does not necessarily have the same properties as the original. The task of model checking using abstraction is difficult because:

1. if the abstraction is too coarse, it will violate the desired properties, even though the concrete system may be correct (“false negative”), and
2. if the abstraction is too fine, it will still be too large to allow efficient verification.

The challenge then is to find a faithful abstraction (which satisfies the desired properties) but is not too large. An alternative approach is to start from the abstraction, and to automatically generate a distributed implementation, by means of *correctness preserving transformations*. One such approach is presented by Hallal and Jaber [1], who give a methodology to automatically synthesize an efficient distributed implementation starting from a *high level global choreography*.

The global choreography describes a set of processes and their interactions together, and from it, we can generate a distributed system. This system can be model checked using Promela, but this suffers from the same problem of state explosion. Thus, we wish to model check the global choreography before generating the distributed implementation. Together with a theory which establishes that the generated implementation *automatically* inherits correctness properties from the choreography, we solve the verification problem.

1.2 Objective

The aim of this thesis is as follows:

1. Given a description of a global choreography, represent it as a Kripke structure and model check this structure with respect to a specification written in the temporal logic CTL [2].
2. Devise a theory which shows that satisfaction of formulae by the global choreography implies satisfaction of the same (or related) formulae by the implemented system. The formula should be drawn from a suitably interesting sublogic of CTL.

The representation and model checking of Kripke structures will be done using the tool Eshmun [3], which can be downloaded from <http://eshmuntool.blogspot.com/>.

1.3 List of Tasks

In order to achieve these objectives, we shall perform the following tasks:

1. Define a semantics for choreographies. The semantics will be given as a set of structured operational semantics rules, which take a pair consisting of a choreography and state (assignment to variables), and produce a new choreography and state as a result of executing the action described by the rule.
2. Using this semantics, we devise an algorithm for generating the Kripke structure of a choreography. We assume that choreographies are finite-state, so that the Kripke structure can be generated by simulating all possible behaviors of the choreography, until no new behaviors are produced. The resulting Kripke structure can then be model checked in Eshmun.
3. Devise *property-preservation results* which state that properties (expressed in CTL) of the choreography are also properties of the implementation.

Hence model checking of the choreography verifies properties of the implementation, i.e., the generated code. We expect these results to follow from the process of constructing the state-transitions of the components of the distributed implementation, as given in Chapter 4 of [1].

4. Apply the method to many case studies.

Chapter 2

Literature Review

2.1 Model of Concurrent Computation

We use the version of BIP that is given in [1].

In this section, we introduce a component-based framework, inspired from the Behavior Interaction Priority framework (BIP) [4]. In the BIP framework, atomic components communicate through an interaction model defined on the interface ports of the atomic components. Unlike BIP, we distinguish between four types of ports: (1) synchronous send; (2) asynchronous send; (3) asynchronous receive; and (4) internal ports. In BIP, all ports have the same type that only allow to build multiparty interactions. The new port types allow to (1) easily model distributed system communication models; (2) provide efficient code generation, under some constraints, that does not require to build controllers to handle conflicts between multiparty interactions. For the sake of simplicity, we omit variables from atomic components at this stage. Formally, a port is defined as follows.

Definition 1 (Port). *A port p consists of the following elements:*

- a port identifier p ;
- its data type $p.dtype \in \{\text{int}, \text{str}, \text{bool}, \dots\}$; and
- its communication type denoted by $p.c\text{type}$ ranging in the set $\{\text{ss}, \text{as}, \text{r}, \text{in}\}$, where **ss** (resp. **as**, **r**, **in**) denotes a synchronous send (resp. asynchronous send, receive, internal) communication type.

Moreover, a receive port p has field $p.\text{buff} \in \mathbb{N}$ denoting the number of signals/-data pending on that port.

Given a port p , we define the predicate $\text{isSSend}(p)$ (resp., isASend , isRecv , isInternal) that holds **true** iff (the communication type of) p is a synchronous send (resp., asynchronous send, receive, internal) port.

Atomic components are the main computation blocks. An atomic component is defined as follows.

Definition 2 (Atomic Component). *An atomic component B_i is defined as a tuple (P_i, Q_i, T_i) , where (1) P_i is a set of ports; (2) Q_i is a set of states; (3) $T_i \subseteq Q_i \times P_i \times Q_i$ is a set of transitions.*

In the sequel, we consider a system of n atomic components $\{B_i = (P_i, Q_i, T_i)\}_{i=1}^n$. We define the set $\mathcal{P} = \cup_{i=1}^n P_i$ (resp. $\mathcal{P}^{ss} = \{p \mid p \in \mathcal{P} \wedge \text{isSSend}(p)\}$, $\mathcal{P}^{as} = \{p \mid p \in \mathcal{P} \wedge \text{isASend}(p)\}$, $\mathcal{P}^r = \{p \mid p \in \mathcal{P} \wedge \text{isRecv}(p)\}$) of all the ports (resp. synchronous send port, asynchronous send ports, receive ports) of the system. Moreover, we denote \mathcal{P}_i^{ss} (resp. \mathcal{P}_i^{as} , \mathcal{P}_i^r) to be the set of all synchronous send (resp., asynchronous send, receive) ports of atomic component B_i . We also consider that port p_i belongs to component i . Given a state q_i , we consider that all the outgoing ports are enabled.

Synchronization between the atomic components is defined using the notion of interaction.

Definition 3 (Interaction). *An interaction is $a = (p_i, \{p_j\}_{j \in J})$, where $i \notin J$, is defined by (1) its send port p_i (synchronous or asynchronous) that belongs to the send ports of atomic component B_i , i.e., $p_i \in \mathcal{P}_i^{ss} \cup \mathcal{P}_i^{as}$; (2) its receive ports $\{p_j\}_{j \in J}$ each of which belongs to the receive ports of atomic component B_j , i.e., $p_j \in \mathcal{P}_j^r$.*

An interaction $a = (p_i, \{p_j\}_{j \in J})$ is synchronous (resp. asynchronous) interaction iff $\text{isSSend}(p_i)$ (resp. $\text{isASend}(p_i)$).

A composite component consists of several atomic components and a set of interactions. The semantics of a composite component is defined as a labeled transition system where the transitions depend on the interaction types (see Figure ??). First, Equation (2.1) represents synchronous interaction, i.e., $a = (p_i, \{p_j\}_{j \in J})$ and $\text{isSSend}(p_i)$, which requires all the corresponding receive ports to be enabled with no pending messages (their buffers are empty) and which results in updating the states all the involved components simultaneously. Second, Equation (2.2) and Equation (2.4) represent asynchronous interactions. Equation (2.2) represents the asynchronous execution of the send port without requiring the participation of the corresponding receive ports, however, upon its execution it places the data or synchronization notice in the buffers of the corresponding receive ports. Then, (2.3) represents the autonomous execution of receive ports with no empty buffers. Finally, Equation 2.4 represents the autonomous execution of internal ports that only allow to change local state of atomic components.

Definition 4 (Composite Component). *A composite component B is defined by a composition operator parameterized by a set of interactions γ . Component $B = \gamma(B_1, \dots, B_n)$ is a transition system (Q, γ, \rightarrow) , where $Q = \otimes_{i=1}^n Q_i$ and \rightarrow is the least set of transitions satisfying the rules in Figure ??.*

$$\text{synchron-send:} \frac{a = (p_i, \{p_j\}_{j \in J}) \in \gamma \quad \text{isSSend}(p_i) \quad \forall k \in J \cup \{i\} : q_k \xrightarrow{p_k} q'_k \quad \forall k \notin J \cup \{i\} : q_k = q'_k \quad \forall k \in J : p_k.\text{buff} = 0}{(q_1, \dots, q_n) \xrightarrow{a} (q'_1, \dots, q'_n)} \quad (2.1)$$

$$\text{asynchron-send:} \frac{a = (p_i, \{p_j\}_{j \in J}) \in \gamma \quad \text{isASend}(p_i) \quad q_i \xrightarrow{p_i} q'_i \quad \forall k \neq i : q_k = q'_k}{(q_1, \dots, q_n) \xrightarrow{a} (q'_1, \dots, q'_n) \quad \forall j \in J : p_j.\text{buff} := p_j.\text{buff} + 1} \quad (2.2)$$

$$\text{recv:} \frac{q_j \xrightarrow{p_j} q'_j \quad \text{isRecv}(p_j) \quad p_j.\text{buff} > 0 \quad \forall k \neq j : q_k = q'_k}{(q_1, \dots, q_n) \xrightarrow{a} (q'_1, \dots, q'_n) \quad p_j.\text{buff} := p_j.\text{buff} - 1} \quad (2.3)$$

$$\text{internal:} \frac{q_i \xrightarrow{p_i} q'_i \quad \text{isInternal}(p_i) \quad \forall k \neq i : q_k = q'_k}{(q_1, \dots, q_n) \xrightarrow{\epsilon} (q'_1, \dots, q'_n)} \quad (2.4)$$

Figure 2.1: Semantic rules defining the behavior of composite components

Finally, a system is defined as a composite component where we specify the initial states of its atomic components.

Definition 5 (System). *A system is a pair $S = (B, \text{init})$, where $B = \gamma(B_1, \dots, B_n)$ is a composite component and $\text{init} \in \bigotimes_{i=1}^n Q_i$ is the the initial state of B .*

2.2 CTL Syntax and Semantics

We use the temporal logic CTL to specify correctness properties.

Let AP be a set of atomic propositions, including the constants `true` and `false`. We use `true`, `false` as “constant” propositions whose interpretation is always the semantic truth values `tt`, `ff`, respectively. The logic CTL [?, 2] is given by the following grammar:

$$\varphi ::= \text{true} \mid \text{false} \mid p \mid \neg\varphi \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \text{AX}\varphi \mid \text{EX}\varphi \mid \text{A}[\varphi\text{R}\varphi] \mid \text{E}[\varphi\text{R}\varphi]$$

where $p \in AP$, and `true`, `false` are constant propositions with interpretation `tt`, `ff` respectively (i.e., “syntactic” true, false respectively).

The semantics of CTL formulae are defined with respect to a Kripke structure.

Definition 6. A Kripke structure is a tuple $M = (S_0, S, R, L)$ where S is a finite state of states, $S_0 \subseteq S$ is a set of initial states, $R \subseteq S \times S$ is a transition relation, and $L : S \mapsto 2^{AP}$ is a labeling function that associates each state $s \in S$ with a subset of atomic propositions, namely those that hold in the state. State t is a successor of state s in M iff $s, t \in R$.

We assume that a Kripke structure $M = (S_0, S, R, L)$ is total, i.e., $\forall s \in S, \exists s' \in S : (s, s') \in R$. A path in M is a (finite or infinite) sequence of states, $\pi = s_0, s_1, \dots$ such that $\forall i \geq 0 : (s_i, s_{i+1}) \in R$. A fullpath is an infinite path. A state is reachable iff it lies on a path that starts in an initial state. Without loss of generality, we assume in the sequel that the Kripke structure M that is to be repaired does not contain any unreachable states, i.e., every $s \in S$ is reachable.

Definition 7. $M, s \models \varphi$ means that formula φ is true in state s of structure M and $M, s \not\models \varphi$ means that formula φ is false in state s of structure M . We define \models inductively as usual:

1. $M, s \models \mathbf{true}$
2. $M, s \not\models \mathbf{false}$
3. $M, s \models p$ iff $p \in L(s)$ where atomic proposition $p \in AP$
4. $M, s \models \neg\varphi$ iff $M, s \not\models \varphi$
5. $M, s \models \varphi \wedge \psi$ iff $M, s \models \varphi$ and $M, s \models \psi$
6. $M, s \models \varphi \vee \psi$ iff $M, s \models \varphi$ or $M, s \models \psi$
7. $M, s \models \mathbf{AX}\varphi$ iff for all t such that $(s, t) \in R : (M, t) \models \varphi$
8. $M, s \models \mathbf{EX}\varphi$ iff there exists t such that $(s, t) \in R$ and $(M, t) \models \varphi$
9. $M, s \models \mathbf{A}[\varphi\mathbf{R}\psi]$ iff for all fullpaths $\pi = s_0, s_1, \dots$ starting from $s = s_0$:
 $\forall k \geq 0 : (\forall j < k : (M, s_j \not\models \varphi))$ implies $M, s_k \models \psi$
10. $M, s \models \mathbf{E}[\varphi\mathbf{R}\psi]$ iff for some fullpath $\pi = s_0, s_1, \dots$ starting from $s = s_0$:
 $\forall k \geq 0 : (\forall j < k : (M, s_j \not\models \varphi))$ implies $M, s_k \models \psi$

We use $M \models \varphi$ to abbreviate $M, S_0 \models \varphi$. We introduce the abbreviations $\mathbf{A}[\phi\mathbf{U}\psi]$ for $\neg\mathbf{E}[\neg\varphi\mathbf{R}\neg\psi]$, $\mathbf{E}[\phi\mathbf{U}\psi]$ for $\neg\mathbf{A}[\neg\varphi\mathbf{R}\neg\psi]$, $\mathbf{AF}\varphi$ for $\mathbf{A}[\mathbf{true}\mathbf{U}\varphi]$, $\mathbf{EF}\varphi$ for $\mathbf{E}[\mathbf{true}\mathbf{U}\varphi]$, $\mathbf{AG}\varphi$ for $\mathbf{A}[\mathbf{false}\mathbf{R}\varphi]$, $\mathbf{EG}\varphi$ for $\mathbf{E}[\mathbf{false}\mathbf{R}\varphi]$.

Chapter 3

Operational Semantics of Choreographies

3.1 Choreography Grammar

We introduce the grammar of choreographies. The following description is from [1]:

We first introduce the abstract syntax of the global choreography model, which allows for: (1) synchronous and asynchronous communications between interface ports; (2) sequential composition of two choreographies; (3) parallel composition of two choreographies; (4) conditional master branching, i.e., the branching decision is taken by a specific component; (5) conditional master loops, i.e., the loop condition is guided by a specific component. Listing 3.1 depicts the abstract syntax of the choreography model.

Send/receive choreography updates the participating components by adding a transition from the current context and labeling it by the corresponding send or receive port from the choreography.

The binary operator \bullet allows to sequentially compose two choreographies, $\mathbf{ch}_1 \bullet \mathbf{ch}_2$

The binary operator \parallel allows for the parallel compositions of two independent choreographies. Two choreographies are independent if their participating components are disjoint.

Branching allows for the modeling of choice between several choreographies. The choice is made by a specific component (B_i), which depending on its internal state would notify the appropriate components to follow the taken choice (i.e.,

```

ch ::= snd → rcvs : <T> # send/receive
|  $B_i \oplus \{\mathbf{snd}_j : \mathbf{ch}\}_{j \in J}$  # Branching - where  $\mathbf{snd}_j \in \mathcal{P}_i^{ss}$ 
| while(snd) ch end # loop
| ch • ch # sequential
| ch || ch # parallelism
|  $\epsilon$ 

snd ::=  $p_i$  # sender - where  $p_i \in \mathcal{P}_i^{ss} \cup \mathcal{P}_i^{as}$ 

rcvs ::=  $p_i$  |  $p_i$  rcvs # receivers - where  $p_i \in \mathcal{P}_i^r$ 

T ::= bool | int | str

```

Listing 3.1: Abstract syntax of the global choreography model

the corresponding choreography, \mathbf{ch}_l).

Loop allows for the modeling of a conditional repeated choreography \mathbf{ch} . The condition is evaluated by a specific component, which will notify the participants of the choreography to either re-execute it or break.

3.2 Choreography Control Predicates

For the following semantics we use these notations:

$\mathbf{F}_p(\sigma)$: is the state that results from executing a transition labeled with port p in state σ

\mathbf{G} : guard that enables a transition if the state satisfies it

\mathbf{snd}, \mathbf{p} : ports

\mathbf{rcvs} : set of ports

\mathbf{q}_i^j : state i of component j

\mathbf{ch} : choreography

$\mathbf{ch.st}$: set to true after first event of \mathbf{ch}

$\mathbf{ch.end}$: set to true after last event of \mathbf{ch}

$\mathbf{ch.in}$: $\mathbf{ch.st} \wedge \neg \mathbf{ch.end}$

\mathbf{fires} : \mathbf{ch} fires p iff \mathbf{ch} executes a transition labelled with port p , i.e., $\mathbf{snd} \rightarrow \mathbf{rcvs}$ where $p = \mathbf{snd}$ or $p \in \mathbf{rcvs}$

\widehat{p} : port p is applied

ϵ : empty choreography

σ : state of the choreography

\perp : error state

\uplus : disjoint union operator, used between states

\bullet : sequential composition operator, execute one choreography and then another

\parallel : parallel composition operator, execute two independent (i.e., do not share components) choreographies in parallel

\oplus : branching composition operator, execute only one of the choreography options if its guard is satisfied

3.3 Operational Semantics

To express a notion of correct implementation of choreographies, we need a semantics for choreographies that is independent of the implementation. We present such a semantics, as a set of structured operational semantics rules.

3.3.1 Sequential

$$\text{sequential1: } \frac{(ch_1, \sigma) \xrightarrow{p} (ch'_1, \sigma')}{(ch_1 \bullet ch_2, \sigma) \xrightarrow{p} (ch'_1 \bullet ch_2, \sigma')} \sigma' = F_p(\sigma) \quad (3.1)$$

Equation (3.1) represents sequential choreographies. When ch_1 fires port p , state σ gets updated to σ' under function F and the choreography is updated to the next event, ch_1 . For $ch_1 \bullet ch_2$, if ch_1 is not the empty choreography, the current event of ch_1 applies port p , the state and choreography are updated accordingly to $(ch'_1 \bullet ch_2, \sigma')$.

$$\text{sequential2: } \frac{}{(\epsilon \bullet ch, \sigma) \rightarrow (ch, \sigma)} \quad (3.2)$$

Equation (3.2) represents the end case for sequential choreographies. When the empty choreography is sequenced by another choreography $(\epsilon \bullet ch, \sigma)$, simply the pair is updated to the next choreography (ch, σ) .

3.3.2 Parallel

$$\text{parallel1:} \frac{(ch_1, \sigma_1) \xrightarrow{p} (ch'_1, \sigma'_1)}{(ch_1 \parallel ch_2, \sigma_1 \uplus \sigma_2) \xrightarrow{p} (ch'_1 \parallel ch_2, \sigma'_1 \uplus \sigma_2)} \sigma'_1 = F_p(\sigma_1) \quad (3.3)$$

Equation (3.3) represents parallel choreographies. When ch_1 fires port p , state σ_1 gets updated to σ'_1 under function F and the choreography is updated to the next event, ch_1 . The state of parallel choreographies is represented by the disjoint of the states of each of the involved choreographies. The choreography that is currently applying its port, updates its state. For $ch_1 \parallel ch_2$, if ch_1 is not the empty choreography, the current event of ch_1 applies port p , the state and choreography are updated accordingly $(ch'_1 \parallel ch_2, \sigma'_1 \uplus \sigma_2)$.

$$\text{parallel2:} \frac{}{(\epsilon \parallel ch, \sigma) \rightarrow (ch, \sigma)} \quad (3.4)$$

Equation (3.4) represents the end case for parallel choreographies. When the empty choreography is paralleled with another choreography $(\epsilon \parallel ch, \sigma)$, simply the pair is updated to the next choreography (ch, σ) .

3.3.3 Branching

$$\text{branching1:} \frac{}{(B_i \oplus G_j \& \text{snd}_j : ch_j, \sigma) \xrightarrow{\text{snd}_j} (ch_j, \sigma')} \sigma \models G_j, \sigma' = F_{\text{snd}_j}(\sigma) \quad (3.5)$$

Equation (3.5) represents the branching choreographies. If the state σ satisfies guard G_j , then it is possible to fire the port snd_j that G_j is guarding and to go to the respective choreography. The state is updated accordingly.

$$\text{branching2:} \frac{}{(B_i \oplus G_j \& \text{snd}_j : ch_j, \sigma) \rightarrow (\epsilon, \perp)} \sigma \not\models G_j \forall j \in J \quad (3.6)$$

Equation (3.6) represents an edge case. If the state σ satisfies none of the guards G_j , then move to the empty choreography and the error state.

3.3.4 While

$$\text{while:} \frac{}{(while(G \& \text{snd}) ch \text{ end}, \sigma) \xrightarrow{\text{snd}} (ch \bullet while(G \& \text{snd}), \sigma')} \sigma \models G, \sigma' = F_{\text{snd}}(\sigma) \quad (3.7)$$

Equation (6.4) represents the while loop of choreographies. If the state σ satisfies guard G , then apply the snd_j that G is guarding and apply choreography ch sequenced by the loop again, the state is updated accordingly.

$$\text{whileEpsilon:} \frac{}{(while(G \& \text{snd}) ch \text{ end}, \sigma) \rightarrow (\epsilon, \sigma)} \sigma \not\models G \quad (3.8)$$

Equation (3.8) represents the end case for while loop of choreographies. If the state σ does not satisfy the guard G , go to the empty choreography.

3.3.5 Send-Receive

Asynchronous Send

$$\text{sndRcvAsynch1:} \frac{}{(snd \rightarrow rcvs, \sigma) \xrightarrow{snd} (rcvs, \sigma')} \sigma' = F_{snd}(\sigma) \quad (3.9)$$

If the sender is free, it will apply its port and update the state.

$$\text{sndRcvAsynch2:} \frac{}{(rcvs, \sigma) \xrightarrow{p} (rcvs - p, \sigma')} p \in rcvs, \sigma' = F_p(\sigma) \quad (3.10)$$

Once a receiver is free, it applies its port and updates the state.

$$\text{sndRcvAsynch3:} \frac{}{(p, \sigma) \xrightarrow{p} (\epsilon, \sigma')} \sigma' = F_p(\sigma) \quad (3.11)$$

Once the last receiver is free, it applies its port and updates the state. Go to the empty choreography.

Synchronous Send

$$\text{sndRcvSynch:} \frac{\text{snd} \cup \text{rcvs} = p_1 \dots p_n,}{(snd \rightarrow rcvs, \sigma) \xrightarrow{P_1 \dots P_n} (\epsilon, \sigma')} \sigma' = F_{p_1}(F_{p_2}(\dots F_{p_n}(\sigma)) \dots) \quad (3.12)$$

The sender and receivers must be free, each applies its port and updates the state. Note that the resulting state σ' results from applying all of the update functions, for all of the ports, since the transition is synchronous. These update functions can be applied in any order, since they modify disjoint parts of the state.

Chapter 4

Kripke Generation

4.1 How to Write a Choreography

The following are rules on how the actual syntax of the input text file for generating the Kripke structure of a choreography should be.

Send-Receive : $B.S > S.R$, or $S.S > B1.R, B2.R$

Sequential : $B.S > S.R * S.S > B1.R, B2.R$

Loop : $\text{while}(B2.C) B1.C > Bk.InfR \wedge Bk.InfS > B1.R, B2.R \wedge B2.S > B1.R$
Note: inside loops we use '^' instead of '*' to mean sequential

Nested Loops : In order to have nested loops, the interior loop must be written in a different choreography

$CH1 = \text{while}(N1.C) B1.S > S.R \wedge CH2 \wedge B2.S > S.R$

$CH2 = \text{while}(N2.C) H2.R > Z4.A \wedge B3.S > S.R \wedge B2 > C2.R$

Branching : $B1 + CH1$, E E is an empty choreography where it is possible to "skip" the choreography

Branching inside Loops : In order to have branching inside loops, the branching must be written in a different choreography

$CH4 = \text{while}(h1.te) P.R > I.PO \wedge CH7$

$CH5 = ZR1.S > B2.R$

$CH6 = ZR2.S > B2.R$

$CH7 = ZZ > RR * Br.1 + CH5, CH6, E$

Parallel : Parallel choreographies are written in their own choreography and contain only sequential events

$CH4 = CH5 || CH6$

$CH5 = B2.MS > Bk.MR2 * Bk.MS2 > S.R$

$CH6 = B1.MS > Bk.MR1 * Bk.MS1 > S.R$

Comments : Start the line with 2 dashes "--" in order to ignore the line

4.2 Three-Process Example

We show an example of the generating algorithm.

4.2.1 The Choreography

Given 3 processes: P_1, P_2 , and P_3 . P_1 either asks P_2 or P_3 for information. They reply with the answer. Finally, all processes terminate

Listing 4.1: Three Process Example

```
CH = P1 ⊕ {CH1, CH2} • CH3
CH1 = P1.S → P2.R • P2.S → P1.R
CH2 = P1.S → P3.R • P3.S → P1.R
CH3 = P1 → φ || P2 → φ || P3 → φ
```

4.2.2 Input File

```
-- This is a comment. A comment must start with 2 dashes "--"
-- and be at the start of a line
-- Each choreography is written on a single line.
-- The first choreography written will be the global choreography
-- The first event in the global choreography is the start state
-- Sequential events are delimited by '*'
CH = P1 + {CH1, CH2} * CH3
CH1 = P1.S > P2.R * P2.S > P1.R
CH2 = P1.S > P3.R * P3.S > P1.R
-- Parallel choreographies are delimited by "||"
-- Parallel choreographies are written in their own choreography
-- Parallel choreographies contain sequential events only
CH3 = CH4 || CH5 || CH6
-- END represents the termination of a process
CH4 = P1 > END
CH5 = P2 > END
CH6 = P3 > END
```

4.2.3 Kripke Structure Generation

The following is the Kripke structure generated from the Three-Process-Example choreography. This was automatically generated by our implementation.

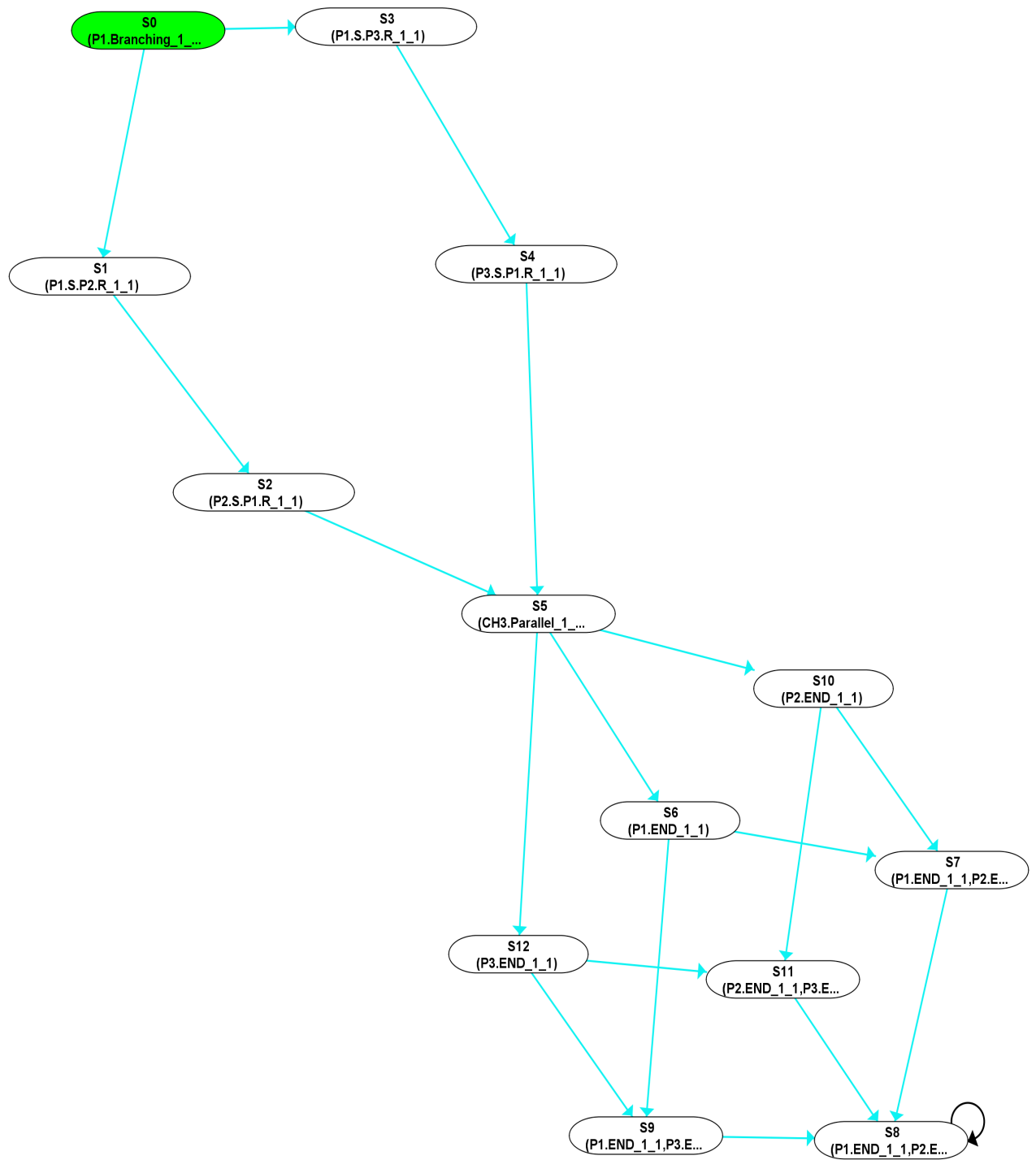


Figure 4.1: Three-Process example - Kripke Structure

4.2.4 Label Definitions

We have 3 components:

- Process 1
- Process 2
- Process 3

CTL formula are built up from atomic propositions, the usual boolean connectives, and temporal modalities. See section 2.2 and [2] for details. Each atomic proposition belongs to exactly one process. We make the convention that an atomic proposition belongs to a process if the last digit in the name of the atomic proposition is the index of the process.

The following shows the atomic propositions (comma separated) each state contains. The atomic propositions were auto generated by *name_i_1* were name is the concatenation of the elements of an event dot separated, *i* is the counter so that the proposition is unique in case repetition of name, and 1 is to indicate that the atomic proposition belongs to process 1 (the global choreography).

```
states:
S0:P1.Branching_1_1
S1:P1.S.P2.R_1_1
S2:P2.S.P1.R_1_1
S3:P1.S.P3.R_1_1
S4:P3.S.P1.R_1_1
S5:CH3.Parallel_1_1
S6:P1.END_1_1
S7:P1.END_1_1,P2.END_1_1
S8:P1.END_1_1,P2.END_1_1,P3.END_1_1
S9:P1.END_1_1,P3.END_1_1
S10:P2.END_1_1
S11:P2.END_1_1,P3.END_1_1
S12:P3.END_1_1
```

We describe each atomic proposition:

P1.Branching_1_1 : Branching point where P1 either asks P2 or P3 for information

P1.S.P2.R_1_1 : P1 asks P2 for information

P2.S.P1.R_1_1 : P2 replies to P1

P1.S.P3.R_1_1 : P1 asks P3 for information

P3.S.P1.R_1_1 : P3 replies to P1

CH3.Parallel_1_1 : Parallel point where system terminates

P1.END_1_1 : P1 terminates

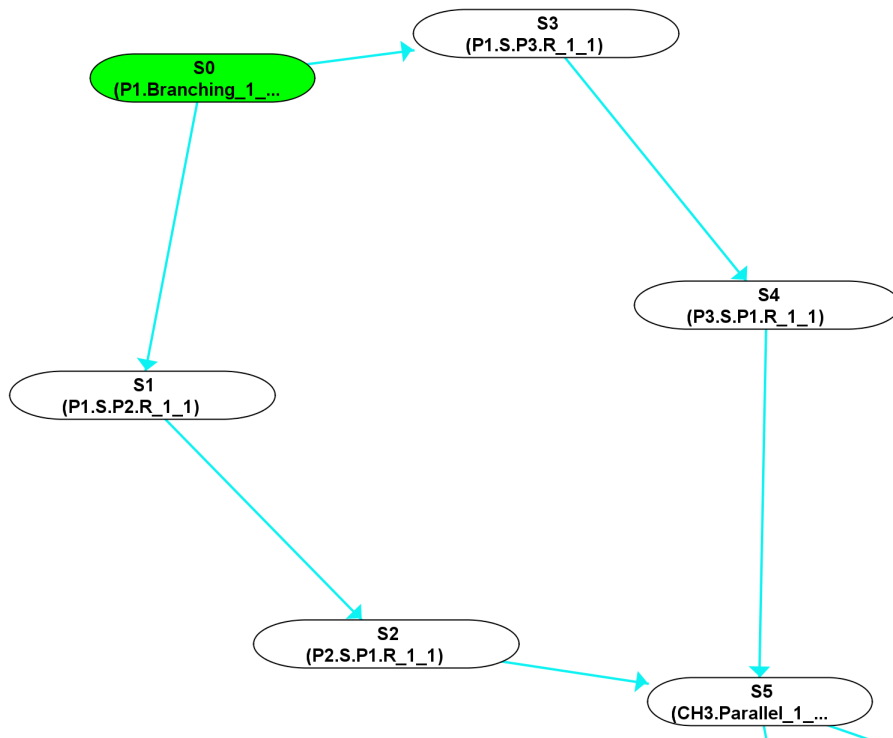
P2.END_1_1 : P2 terminates

P3.END_1_1 : P3 terminates

4.2.5 Explanation of the Kripke Structure

We explain below the various segments of the Kripke structure of the Three-Process example.

Request Information

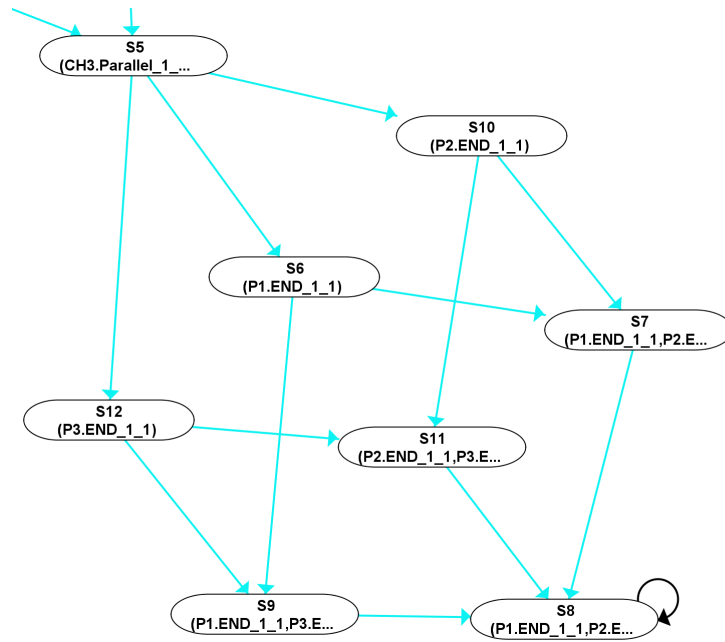


S0: Branching choice, P1 chooses which process to request

Case1: $S0 \rightarrow S1 \rightarrow S2$ P1 asks P2 for information and P2 replies

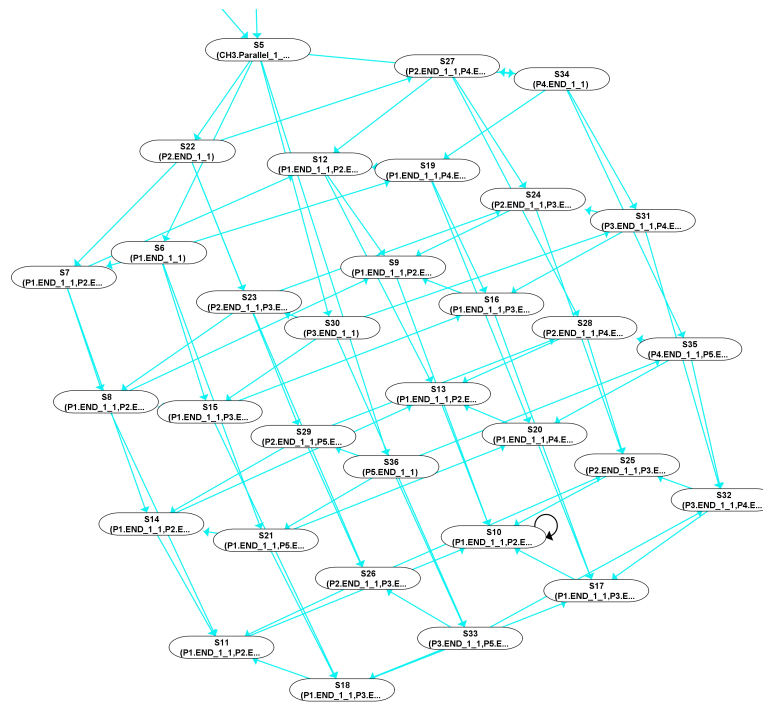
Case2: $S0 \rightarrow S3 \rightarrow S4$ P1 asks P3 for information and P3 replies

System Termination



All the different way for the three processes to terminate in parallel.

System Termination: 5 Processes



However, if five processes terminate in parallel, the number of unique state grows exponentially. Thus choreographies are prone to state explosion and we need to define correctness semantics for choreographies.

Chapter 5

Correctness of choreographies

Since choreographies include the parallel composition operator, even the verification of a choreography can be subject to state-explosion. Hence we present a set of inductive rules which enable the deduction of correctness properties of choreographies from correctness properties of the smaller choreographies from which they are built. This allows us to avoid state explosion in verifying properties of choreographies. We deal with the following

- $\text{AG}(\varphi)$
- $\text{AG}(\varphi \rightarrow \text{AF}(\psi))$

Where φ and ψ are purely propositional

5.1 Sequential

In chapter 4 in [1], sequential semantics is defined by (i) applying ch_1 ; (ii) notifying the start of ch_2 ; and finally (iii) applying ch_2 .

$$\text{SeqCorrectness1: } \frac{ch_1 \models \text{AG}(\varphi)}{ch_1 \bullet ch_2 \models \text{A}[\varphi \text{ W } ch_1.\text{end}]} \quad (5.1)$$

Given for all paths, for every state in ch_1 , φ is satisfied.

Conclude $ch_1 \bullet ch_2 \models$ for all paths φ holds weak until ch_1 ends.

$$\text{SeqCorrectness2: } \frac{ch_2 \models \text{AG}(\varphi)}{ch_1 \bullet ch_2 \models \text{AG}(ch_2.\text{st} \implies \text{AG}(\varphi))} \quad (5.2)$$

Given for all paths, for every state in ch_2 , φ is satisfied.

Conclude $ch_1 \bullet ch_2 \models$ for all paths, for every state, if ch_2 starts, then for all paths, for every state in ch_2 , φ holds.

$$\text{SeqCorrectness3: } \frac{ch_1 \models \text{AG}(\varphi \implies \text{AF}(\psi))}{ch_1 \bullet ch_2 \models \text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \text{ W } ch_1.\text{end}]} \quad (5.3)$$

Given for all paths, for every state s in ch_1 , if φ is satisfied, then for all paths from s , finally ψ will be satisfied.

Conclude $ch_1 \bullet ch_2 \models$ for all paths [for all paths, for every state s , if φ is satisfied, then for all paths from s , finally ψ is satisfied weak until ch_1 ends].

$$\text{SeqCorrectness4: } \frac{ch_2 \models \text{AG}(\varphi \implies \text{AF}(\psi))}{ch_1 \bullet ch_2 \models \text{AG}[ch_2.st \implies \text{AG}(\varphi \implies \text{AF}(\psi))]} \quad (5.4)$$

Given for all paths, for every state s in ch_2 , if φ is satisfied, then for all paths from s , finally ψ will be satisfied.

Conclude $ch_1 \bullet ch_2 \models$ for all paths, for every state s , if ch_2 starts, then for all paths from s , for every state, if φ is satisfied, then finally ψ is satisfied.

$$\text{SeqCorrectness5: } \frac{ch_1 \models \text{AG}(\varphi_1 \implies \text{AF}(ch_1.end)), ch_2 \models \text{AG}(ch_2.st \implies \text{AF}(\varphi_2))}{ch_1 \bullet ch_2 \models \text{AG}(\varphi_1 \implies \text{AF}(\varphi_2))} \quad (5.5)$$

Given for all paths, for every state s in ch_1 , if φ_1 is satisfied, then for all paths from s , finally $ch_1.end$ will be satisfied.

Given for all paths, for every state s in ch_2 , if $ch_2.st$ is satisfied, then for all paths from s , finally ψ_2 will be satisfied.

Conclude $ch_1 \bullet ch_2 \models$ for all paths, for every state s , if φ_1 is satisfied, then for all paths from s , finally φ_2 is satisfied.

5.2 Parallel

In chapter 4, the binary operator \parallel allows for the parallel compositions of two independent choreographies. Two choreographies are independent if their participating components are disjoint. Thus, if ch_1 and ch_2 are applied in parallel, the properties of each choreography hold together.

$$\text{ParCorrectness1: } \frac{ch_1 \models \text{AG}(\varphi_1), ch_2 \models \text{AG}(\varphi_2)}{ch_1 \parallel ch_2 \models \text{AG}(\varphi_1) \wedge \text{AG}(\varphi_2)} \quad (5.6)$$

Given for all paths, for every state in ch_1 , φ_1 is satisfied.

Given for all paths, for every state in ch_2 , φ_2 is satisfied.

Conclude $ch_1 \parallel ch_2 \models$ for all paths, for every state, φ_1 and φ_2 hold.

$$\text{ParCorrectness2: } \frac{ch_1 \models \text{AG}(\varphi_1 \implies \text{AF}(\psi_1)), ch_2 \models \text{AG}(\varphi_2 \implies \text{AF}(\psi_2))}{ch_1 \parallel ch_2 \models \text{AG}(\varphi_1 \implies \text{AF}(\psi_1)) \wedge \text{AG}(\varphi_2 \implies \text{AF}(\psi_2))} \quad (5.7)$$

Given for all paths, for every state s in ch_1 , if φ_1 is satisfied, then for all paths from s , finally ψ_1 will be satisfied.

Given for all paths, for every state s in ch_2 , if φ_2 is satisfied, then for all paths from s , finally ψ_2 will be satisfied.

Conclude $ch_1 \parallel ch_2 \models$ for all paths, for every state s , if φ_1 holds then for all paths from s , finally ψ_1 holds and if φ_2 holds then for all paths, finally ψ_2 holds.

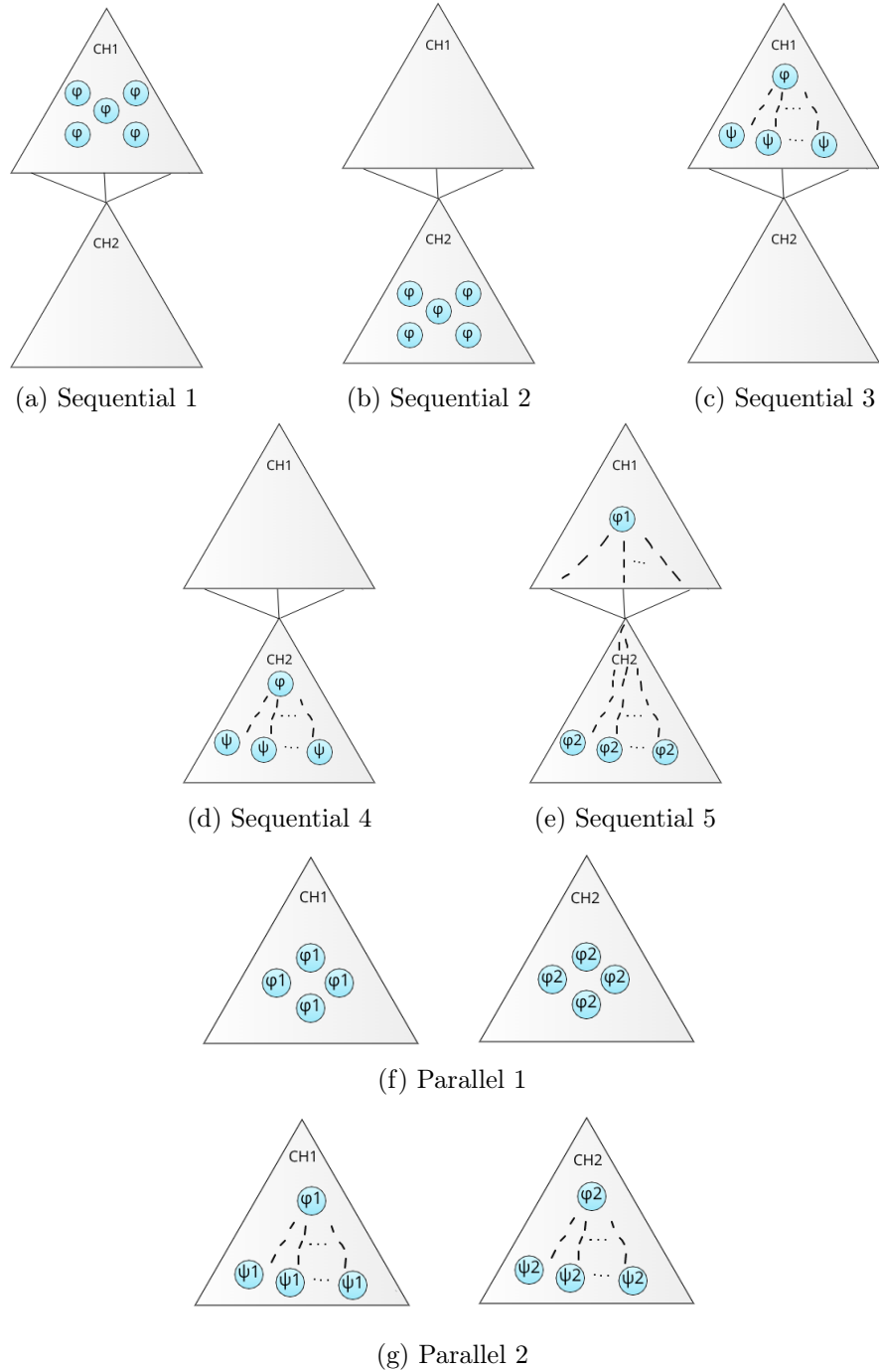


Figure 5.1: Sequential and Parallel Correctness

5.3 Branching

Branching allows for the modeling of choice between several choreographies. The \oplus operator allows a specific component to select from a set of choreographies. Note that it is required to notify all the participants of a choice and not only the start components.

$$\text{BranCorrectness1:} \frac{ch_j \models \text{AG}(\varphi_j), j \in J}{(B_i \oplus G_j \& \widehat{snd}_j : ch_j) \models \text{AG}(G_j \wedge \widehat{snd}_j \implies \text{AG}(\varphi_j))} \quad (5.8)$$

Given for all paths, for every state in ch_j , φ_j is satisfied.

Conclude $(B_i \oplus G_j \& \widehat{snd}_j : ch_j) \models$ for all paths, for every state s , if guard G_j is satisfied and \widehat{snd}_j , i.e. this branch is selected, then for all paths from s , for every state, φ_j holds.

$$\text{BranCorrectness2:} \frac{ch_j \models \text{AG}(\varphi_j \implies \text{AF}(\psi_j)), j \in J}{(B_i \oplus G_j \& \widehat{snd}_j : ch_j) \models \text{AG}(G_j \wedge \widehat{snd}_j \implies \text{AG}(\varphi_j \implies \text{AF}(\psi_j)))} \quad (5.9)$$

Given for all paths, for every state s in ch_j , if φ_j is satisfied, then for all paths from s , finally ψ_j is satisfied.

Conclude $(B_i \oplus G_j \& \widehat{snd}_j : ch_j) \models$ for all paths, for every state, if guard G_j is satisfied and \widehat{snd}_j , i.e. this branch is selected, then for all paths, for every state s , if φ_j is satisfied, then for all paths from s , finally ψ_j is satisfied.

5.4 While

Loop allows for the modeling of a conditional repeated choreography ch . The condition is evaluated by a specific component, which will notify, through the port p_i , the participants of the choreography to either re-execute it or break.

$$\text{whileCorrectness1:} \frac{ch \models \text{AG}(\varphi)}{\text{while}(G \& \widehat{snd}) ch \text{ end} \models \text{AG}(G \wedge \widehat{snd} \implies \text{AG}(\varphi))} \quad (5.10)$$

Given for all paths, for every state in ch , φ is satisfied.

Conclude $\text{while}(G \& \widehat{snd}) ch \text{ end} \models$ for all paths, for every state s , if guard G is satisfied and \widehat{snd} , i.e. we are in the loop, then for all paths from s , for every state, φ holds.

$$\text{whileCorrectness2:} \frac{ch \models \text{AG}(\varphi \implies \text{AF}(\psi))}{\text{while}(G \& \widehat{snd}) ch \text{ end} \models \text{AG}(G \wedge \widehat{snd} \implies \text{AG}(\varphi \implies \text{AF}(\psi)))} \quad (5.11)$$

Given for all paths, for every state s in ch , if φ is satisfied, then for all paths from s , finally ψ is satisfied.

Conclude $\widehat{while}(G \& snd) \ ch \ end \models$ for all paths, for every state, if guard G is satisfied and \widehat{snd} , i.e. we are in the loop, then for all paths, for every state s , if φ is satisfied, then for all paths from s , finally ψ is satisfied.

5.5 Send-Receive

5.5.1 Asynchronous Send

Send-receive choreography updates the participating components by adding a transition from the current context and labeling it by the corresponding send or receive port from the choreography.

$$\text{sndRecvAsynch:} \frac{}{snd \rightarrow rcvs \models \text{AG}(q_1^1 \wedge \dots \wedge q_1^n \implies \text{AF}(q_2^1) \wedge \dots \wedge \text{AF}(q_2^n))} \quad (5.12)$$

Only the starter component must be free in order to execute, and whenever a receiver component is free, it will execute. Thus we can say that then $\text{AF}(q_2^1) \wedge \dots \wedge \text{AF}(q_2^n)$.

5.5.2 Synchronous Send

$$\text{sndRecvSynch:} \frac{}{snd \rightarrow rcvs \models \text{AG}(q_1^1 \wedge \dots \wedge q_1^n \implies \text{AF}(q_2^1 \wedge \dots \wedge q_2^n))} \quad (5.13)$$

All components must be free in order to execute, thus if $q_1^1 \wedge \dots \wedge q_1^n$ are the current states of the respective components, then $\text{AF}(q_2^1 \wedge \dots \wedge q_2^n)$.

Chapter 6

Correctness of Distributed Implementations of Choreographies

6.1 Implementation of Choreographies

We define implementation as a function $I: \text{Choreographies} \rightarrow \text{BIP}$. I is defined by structural induction over the definition of a choreography. We write $I(ch) = \llbracket ch \rrbracket$ to indicate that $\llbracket ch \rrbracket$ is the BIP system that implements ch .

6.1.1 Sequential

If $I(ch_1) = \llbracket ch_1 \rrbracket$, $I(ch_2) = \llbracket ch_2 \rrbracket$ then $I(ch_1 \bullet ch_2)$ consists of $\llbracket ch_1 \rrbracket$ then for all components common to $\llbracket ch_1 \rrbracket$, $\llbracket ch_2 \rrbracket$ insert $\llbracket ch_{\text{synch}} \rrbracket = \text{snd} \rightarrow \text{rcvs}$ synchronous with interaction $a = \text{snd} \rightarrow \text{rcvs}$ that synchronizes the end of $\llbracket ch_1 \rrbracket$ with the start of $\llbracket ch_2 \rrbracket$. In this, and all subsequent rules, the semantics of the execution of an interaction is given by section 2.1.

$$\text{SequentialImp: } \frac{I(ch_1) = \llbracket ch_1 \rrbracket, I(ch_2) = \llbracket ch_2 \rrbracket}{I(ch_1 \bullet ch_2) = \llbracket ch_1 \bullet ch_2 \rrbracket = \llbracket ch_1 \rrbracket \rightarrow \llbracket ch_{\text{synch}} \rrbracket \rightarrow \llbracket ch_2 \rrbracket} \quad (6.1)$$

The following example is from [1]. The figure was modified.

Example 1 (Sequential composition). *Figure 6.1 shows an abstract example on how to transform sequential composition of two choreographies, $\mathbf{ch}_1 \bullet \mathbf{ch}_2$, into an initial system consisting of five components. Here we only consider components that are involved in those choreographies, where (1) components b_1, b_2, b_3 and b_4 are involved in choreography \mathbf{ch}_1 ; and (2) components b_1, b_2, b_3 and b_5 are involved in choreography \mathbf{ch}_2 . Note, components that are not involved are kept unchanged. The transformation requires to: (1) apply first choreography \mathbf{ch}_1 to its participated components (i.e., b_1, b_2, b_3 and b_4); (2) synchronize the end of*

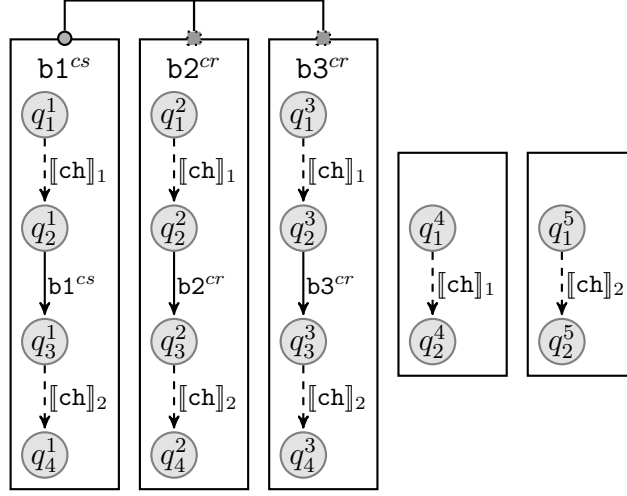


Figure 6.1: Sequential composition transformation

choreography ch_1 (e.g., b_1) with the start of choreography ch_2 (e.g., b_2 and b_3). To do so, we create a synchronous send port to one of the end components of ch_1 (e.g., b_1^{cs}) and connect it to all the remaining end components of ch_1 (e.g., \emptyset and the start components of ch_2 (e.g., b_2 and b_3); finally (3) we apply choreography ch_2 .

6.1.2 Parallel

If $I(\text{ch}_1) = \llbracket \text{ch}_1 \rrbracket$, $I(\text{ch}_2) = \llbracket \text{ch}_2 \rrbracket$ then $I(\text{ch}_1 \parallel \text{ch}_2)$ consists of $\llbracket \text{ch}_1 \rrbracket$ in parallel with $\llbracket \text{ch}_2 \rrbracket$. The components are disjoint.

$$\text{ParallelImp: } \frac{I(\text{ch}_1) = \llbracket \text{ch}_1 \rrbracket, I(\text{ch}_2) = \llbracket \text{ch}_2 \rrbracket}{I(\text{ch}_1 \parallel \text{ch}_2) = \llbracket \text{ch}_1 \rrbracket \parallel \llbracket \text{ch}_2 \rrbracket = \phi(\llbracket \text{ch}_1 \rrbracket, \llbracket \text{ch}_2 \rrbracket)} \quad (6.2)$$

$\phi(\llbracket \text{ch}_1 \rrbracket, \llbracket \text{ch}_2 \rrbracket)$ definition 4, Section 2.1 The following example is from [1]. The figure was modified.

Example 2 (Parallel Composition). *Figure 6.2 shows an abstract example on how to transform parallel composition of two choreographies, $\text{ch}_1 \parallel \text{ch}_2$, into an initial system consisting of five components. Here, we consider that ch_1 (resp. ch_2) involves components B_1 and B_2 (resp. B_3 and B_4).*

6.1.3 Branching

$$\text{branchingImp: } \frac{I(\text{ch}_j) = \llbracket \text{ch}_j \rrbracket, j \in J}{I(B_i \oplus G_j \& \text{snd}_j : \text{ch}_j) = B_i \oplus G_j \& \text{snd}_j : \llbracket \text{ch}_j \rrbracket} \quad (6.3)$$

If $I(\text{ch}_j) = \llbracket \text{ch}_j \rrbracket \forall j \in J$, then $I(B_i \oplus G_j \& \text{snd}_j : \text{ch}_j)$ consists of $B_i \oplus G_j \& \text{snd}_j : \llbracket \text{ch}_j \rrbracket$.

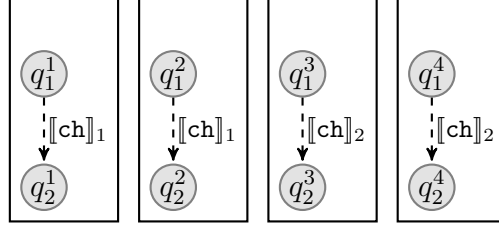


Figure 6.2: Parallel composition transformation

6.1.4 While

$$\text{while: } \frac{I(ch) = \llbracket ch \rrbracket}{I(\text{while}(G\&snd) \ ch \ end) = \text{while}(G\&snd) \llbracket ch \rrbracket \ end} \quad (6.4)$$

If $I(ch) = \llbracket ch \rrbracket$, then $I(\text{while}(G\&snd) \ ch \ end)$ consists of $\text{while}(G\&snd) \llbracket ch \rrbracket \ end$.

6.1.5 Send-Receive

$$\text{sndRcvImp: } \frac{}{I(snd \rightarrow rcvs) = \llbracket snd \rightarrow rcvs \rrbracket} \quad (6.5)$$

$I(snd \rightarrow rcvs)$ consists of $\llbracket snd \rightarrow rcvs \rrbracket$.

6.2 Correctness

\hat{a} is an atomic proposition that is true immediately after a has executed.

6.2.1 Sequential

$$\text{SeqImpCorrectness1: } \frac{\llbracket ch_1 \rrbracket \models \text{AG}(\varphi)}{\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\varphi \ \text{W} \ \hat{a}]} \quad (6.6)$$

Given for all paths, for every state in $\llbracket ch_1 \rrbracket$, φ is satisfied.

Conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models$ for all paths φ holds weak until \hat{a} . Here \hat{a} means the end of $\llbracket ch_1 \rrbracket$.

$$\text{SeqImpCorrectness2: } \frac{\llbracket ch_2 \rrbracket \models \text{AG}(\varphi)}{\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}(\hat{a} \implies \text{AG}(\varphi))} \quad (6.7)$$

Given for all paths, for every state in $\llbracket ch_2 \rrbracket$, φ is satisfied.

Conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models$ for all paths, for every state s , if \hat{a} starts, then for all paths from s , for every state in ch_2 , φ holds. Here \hat{a} means the start of $\llbracket ch_2 \rrbracket$.

$$\text{SeqImpCorrectness3: } \frac{\llbracket ch_1 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))}{\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \ \text{W} \ \hat{a}]} \quad (6.8)$$

Given for all paths, for every state s in $\llbracket ch_1 \rrbracket$, if φ is satisfied, then for all paths from s , finally ψ will be satisfied.

Conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models$ for all paths [for all paths, for every state s , if φ is satisfied, then for all paths from s , finally ψ is satisfied weak until \hat{a}]. Here \hat{a} means the end of $\llbracket ch_1 \rrbracket$.

$$\text{SeqImpCorrectness4: } \frac{\llbracket ch_2 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))}{\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}[\hat{a} \implies \text{AG}(\varphi \implies \text{AF}(\psi))]} \quad (6.9)$$

Given for all paths, for every state s in $\llbracket ch_2 \rrbracket$, if φ is satisfied, then for all paths from s , finally ψ will be satisfied.

Conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models$ for all paths, for every state s , if \hat{a} , then for all paths from s , for every state, if φ is satisfied, then finally ψ_2 is satisfied. Here \hat{a} signals the start of ch_2 .

$$\text{SeqImpCorrectness5: } \frac{\llbracket ch_1 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\hat{a})), \llbracket ch_2 \rrbracket \models \text{AG}(\hat{a} \implies \text{AF}(\psi))}{\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))} \quad (6.10)$$

Given for all paths, for every state in $\llbracket ch_1 \rrbracket$, if φ is satisfied, then for all paths, finally \hat{a} will be satisfied. Here \hat{a} means the end of $\llbracket ch_1 \rrbracket$.

Given for all paths, for every state in $\llbracket ch_2 \rrbracket$, if \hat{a} is satisfied, then for all paths, finally ψ will be satisfied.

Conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models$ for all paths, for every state, if φ is satisfied, then for all paths, finally ψ is satisfied.

6.2.2 Parallel

$$\text{ParImpCorrectness1: } \frac{\llbracket ch_1 \rrbracket \models \text{AG}(\varphi_1), \llbracket ch_2 \rrbracket \models \text{AG}(\varphi_2)}{\llbracket ch_1 \parallel ch_2 \rrbracket \models \text{AG}(\varphi_1) \wedge \text{AG}(\varphi_2)} \quad (6.11)$$

Given for all paths, for every state in $\llbracket ch_1 \rrbracket$, φ_1 is satisfied.

Given for all paths, for every state in $\llbracket ch_2 \rrbracket$, φ_2 is satisfied.

Conclude $\llbracket ch_1 \parallel ch_2 \rrbracket \models$ for all paths, for every state, φ_1 and φ_2 hold.

$$\text{ParImpCorrectness2: } \frac{\llbracket ch_1 \rrbracket \models \text{AG}(\varphi_1 \implies \text{AF}(\psi_1)), \llbracket ch_2 \rrbracket \models \text{AG}(\varphi_2 \implies \text{AF}(\psi_2))}{\llbracket ch_1 \parallel ch_2 \rrbracket \models \text{AG}(\varphi_1 \implies \text{AF}(\psi_1)) \wedge \text{AG}(\varphi_2 \implies \text{AF}(\psi_2))} \quad (6.12)$$

Given for all paths, for every state s in $\llbracket ch_1 \rrbracket$, if φ_1 is satisfied, then for all paths from s , finally ψ_1 will be satisfied.

Given for all paths, for every state s in $\llbracket ch_2 \rrbracket$, if φ_2 is satisfied, then for all paths from s , finally ψ_2 will be satisfied.

Conclude $\llbracket ch_1 \parallel ch_2 \rrbracket \models$ for all paths, for every state s_1 , if φ_1 holds then for all paths from s_1 , finally ψ_1 holds. And for all paths, for every state s_2 , if φ_2 holds then for all paths from s_2 , finally ψ_2 holds.

6.2.3 Branching

$$\text{BranCorrectness1:} \frac{\llbracket ch_j \rrbracket \models \text{AG}(\varphi_j), j \in J}{(B_i \oplus G_j \& \widehat{snd}_j : \llbracket ch_j \rrbracket) \models \text{AG}(G_j \wedge \widehat{snd}_j \implies \text{AG}(\varphi_j))} \quad (6.13)$$

Given for all paths, for every state in $\llbracket ch_j \rrbracket$, φ_j is satisfied.

Conclude $(B_i \oplus G_j \& \widehat{snd}_j : \llbracket ch_j \rrbracket) \models$ for all paths, for every state s , if guard G_j is satisfied and \widehat{snd}_j , i.e. this branch selected, then for all paths from s , for every state, φ_j holds.

$$\text{BranCorrectness2:} \frac{\llbracket ch_j \rrbracket \models \text{AG}(\varphi_j \implies \text{AF}(\psi_j)), j \in J}{(B_i \oplus G_j \& \widehat{snd}_j : \llbracket ch_j \rrbracket) \models \text{AG}(G_j \wedge \widehat{snd}_j \implies \text{AG}(\varphi_j \implies \text{AF}(\psi_j)))} \quad (6.14)$$

Given for all paths, for every state s in ch_j , if φ_j is satisfied, then for all paths from s , finally ψ_j is satisfied.

Conclude $(B_i \oplus G_j \& \widehat{snd}_j : \llbracket ch_j \rrbracket) \models$ for all paths, for every state, if guard G_j is satisfied and \widehat{snd}_j , i.e. this branch is selected, then for all paths, for every state s , if φ_j is satisfied, then for all paths from s , finally ψ_j is satisfied.

6.2.4 While

$$\text{whileImpCorrectness1:} \frac{\llbracket ch \rrbracket \models \text{AG}(\varphi)}{\text{while}(G \& \widehat{snd}) \llbracket ch \rrbracket \text{ end} \models \text{AG}(G \wedge \widehat{snd} \implies \text{AG}(\varphi))} \quad (6.15)$$

Given for all paths, for every state in $\llbracket ch \rrbracket$, φ is satisfied.

Conclude $\text{while}(G \& \widehat{snd}) \llbracket ch \rrbracket \text{ end} \models$ for all paths, for every state s , if guard G is satisfied and \widehat{snd} , i.e. we are in the loop, then for all paths from s , for every state, φ holds.

$$\text{whileImpCorrectness2:} \frac{\llbracket ch \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))}{\text{while}(G \& \widehat{snd}) \llbracket ch \rrbracket \text{ end} \models \text{AG}(G \wedge \widehat{snd} \implies \text{AG}(\varphi \implies \text{AF}(\psi)))} \quad (6.16)$$

Given for all paths, for every state s in $\llbracket ch \rrbracket$, if φ is satisfied, then for all paths from s , finally ψ is satisfied.

Conclude $\text{while}(G \& \widehat{snd}) \llbracket ch \rrbracket \text{ end} \models$ for all paths, for every state, if guard G is satisfied and \widehat{snd} , i.e. we are in the loop, then for all paths, for every state s , if φ is satisfied, then for all paths from s , finally ψ is satisfied.

6.2.5 Send-Receive

Asynchronous Send

Send-receive choreography updates the participating components by adding a transition from the current context and labeling it by the corresponding send or receive port from the choreography.

$$\text{sndRecvsAsynchImp: } \frac{}{\llbracket \text{snd} \rightarrow \text{rcvs} \rrbracket \models \text{AG}(q_1^1 \wedge \dots \wedge q_1^n \implies \text{AF}(q_2^1) \wedge \dots \wedge \text{AF}(q_2^n))} \quad (6.17)$$

Only the starter component must be free in order to execute, and whenever a receiver component is free, it will execute. Thus we can say that then $\text{AF}(q_2^1) \wedge \dots \wedge \text{AF}(q_2^n)$.

Synchronous Send

$$\text{sndRecvsSynchImp: } \frac{}{\llbracket \text{snd} \rightarrow \text{rcvs} \rrbracket \models \text{AG}(q_1^1 \wedge \dots \wedge q_1^n \implies \text{AF}(q_2^1 \wedge \dots \wedge q_2^n))} \quad (6.18)$$

All components must be free in order to execute, thus if $q_1^1 \wedge \dots \wedge q_1^n$ are the current states of the respective components, then $\text{AF}(q_2^1 \wedge \dots \wedge q_2^n)$.

Chapter 7

Correctness of Synthesis Method

7.1 Correctness Theorems

The following theorems show that, if the global choreography satisfy certain formulae, then the implemented system does as well. We deal with sequence only. The theorems are proven by induction on the length of the derivation of $\llbracket ch \rrbracket$ from ch , where $snd \rightarrow rcv$ is the base case. $snd \rightarrow rcv$ and $\llbracket snd \rightarrow rcv \rrbracket$ satisfy the same formulae.

Theorem 1 (Sequential Correctness 1). *If $ch_1 \models \text{AG}(\varphi)$ implies $\llbracket ch_1 \rrbracket \models \text{AG}(\varphi)$ then $ch_1 \bullet ch_2 \models \text{A}[\varphi \text{ W } ch_1.\text{end}]$ implies $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\varphi \text{ W } \hat{a}]$. Here \hat{a} signals end of $\llbracket ch_1 \rrbracket$*

$$\frac{ch_1 \models \text{AG}(\varphi) \implies \llbracket ch_1 \rrbracket \models \text{AG}(\varphi)}{ch_1 \bullet ch_2 \models \text{A}[\varphi \text{ W } ch_1.\text{end}] \implies \llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\varphi \text{ W } \hat{a}]} \quad (7.1)$$

Premises:

1. $ch_1 \models \text{AG}(\varphi) \implies \llbracket ch_1 \rrbracket \models \text{AG}(\varphi)$
2. $ch_1 \bullet ch_2 \models \text{A}[\varphi \text{ W } ch_1.\text{end}]$

Required to prove:

3. $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\varphi \text{ W } \hat{a}]$

From 2 we get:

4. $ch_1 \models \text{AG}(\varphi)$

Modus Ponens using 1 and 4 we get:

5. $\llbracket ch_1 \rrbracket \models \text{AG}(\varphi)$

Finally from 6.6 we conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\varphi_1 \text{ W } \hat{a}]$

Theorem 2 (Sequential Correctness 2). *If $ch_2 \models \text{AG}(\varphi)$ implies $\llbracket ch_2 \rrbracket \models \text{AG}(\varphi)$ then $ch_1 \bullet ch_2 \models \text{AG}(ch_2.st \implies \text{AG}(\varphi))$ implies $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}(\hat{a} \implies \text{AG}(\varphi))$. Here \hat{a} signals start of $\llbracket ch_2 \rrbracket$*

$$\frac{ch_2 \models \text{AG}(\varphi) \implies \llbracket ch_2 \rrbracket \models \text{AG}(\varphi)}{ch_1 \bullet ch_2 \models \text{AG}(ch_2.st \implies \text{AG}(\varphi)) \implies \llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}(\hat{a} \implies \text{AG}(\varphi))} \quad (7.2)$$

Premises:

1. $ch_2 \models \text{AG}(\varphi) \implies \llbracket ch_2 \rrbracket \models \text{AG}(\varphi)$
2. $ch_1 \bullet ch_2 \models \text{AG}(ch_2.st \implies \text{AG}(\varphi))$

Required to prove:

3. $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}(\hat{a} \implies \text{AG}(\varphi))$

From 2 we get:

4. $ch_2 \models \text{AG}(\varphi)$

Modus Ponens using 1 and 4 we get:

5. $\llbracket ch_2 \rrbracket \models \text{AG}(\varphi)$

Finally from 6.7 we conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}(\hat{a} \implies \text{AG}(\varphi_2))$

Theorem 3 (Sequential Correctness 3). *If $ch_1 \models \text{AG}(\varphi \implies \text{AF}(\psi))$ implies $\llbracket ch_1 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))$ then $ch_1 \bullet ch_2 \models \text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \text{ W } ch_1.end]$ implies $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \text{ W } \hat{a}]$. Here \hat{a} signals end of $\llbracket ch_1 \rrbracket$*

$$\frac{ch_1 \models \text{AG}(\varphi \implies \text{AF}(\psi)) \implies \llbracket ch_1 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))}{ch_1 \bullet ch_2 \models \text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \text{ W } ch_1.end] \implies \llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \text{ W } \hat{a}]} \quad (7.3)$$

Premises:

1. $ch_1 \models \text{AG}(\varphi \implies \text{AF}(\psi)) \implies \llbracket ch_1 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))$
2. $ch_1 \bullet ch_2 \models \text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \text{ W } ch_1.end]$

Required to prove:

3. $\text{A}[\text{AG}(\varphi \implies \text{AF}(\psi)) \text{ W } \hat{a}]$

From 2 we get:

$$4. ch_1 \models \text{AG}(\varphi \implies \text{AF}(\psi))$$

Modus Ponens using 1 and 4 we get:

$$5. \llbracket ch_1 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))$$

Finally from 6.8 we conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{A}[\text{AG}(\varphi_1 \implies \text{AF}(\psi_1)) \text{ W } \hat{a}]$

Theorem 4 (Sequential Correctness 4). *If $ch_2 \models \text{AG}(\varphi \implies \text{AF}(\psi))$ implies $\llbracket ch_2 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))$ then $ch_1 \bullet ch_2 \models \text{AG}[ch_2.st \implies \text{AG}(\varphi \implies \text{AF}(\psi))]$ implies $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}[\hat{a} \implies \text{AG}(\varphi \implies \text{AF}(\psi))]$ \hat{a} signals start of $\llbracket ch_2 \rrbracket$*

$$\frac{ch_2 \models \text{AG}(\varphi \implies \text{AF}(\psi)) \implies \llbracket ch_2 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))}{\begin{array}{l} ch_1 \bullet ch_2 \models \text{AG}[ch_2.st \implies \text{AG}(\varphi \implies \text{AF}(\psi))] \\ \implies \llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}[\hat{a} \implies \text{AG}(\varphi \implies \text{AF}(\psi))] \end{array}} \quad (7.4)$$

Premises:

$$1. ch_2 \models \text{AG}(\varphi \implies \text{AF}(\psi)) \text{ implies } \llbracket ch_2 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))$$

$$2. ch_1 \bullet ch_2 \models \text{AG}[ch_2.st \implies \text{AG}(\varphi \implies \text{AF}(\psi))]$$

Required to prove:

$$3. \llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}[\hat{a} \implies \text{AG}(\varphi \implies \text{AF}(\psi))]$$

From 2 we get:

$$4. ch_2 \models \text{AG}(\varphi \implies \text{AF}(\psi))$$

Modus Ponens using 1 and 4 we get:

$$5. \llbracket ch_2 \rrbracket \models \text{AG}(\varphi \implies \text{AF}(\psi))$$

Finally from 6.9 we conclude $\llbracket ch_1 \bullet ch_2 \rrbracket \models \text{AG}[\hat{a} \implies \text{AG}(\varphi_2 \implies \text{AF}(\psi_2))]$

7.2 Three-Process Example Correctness

We give an example of the correctness theorem by going back to the Three-Process example.

$$\begin{array}{l} \text{CH} = P_1 \oplus \{ \text{CH}_1, \text{CH}_2 \} \bullet \text{CH}_3 \\ \text{CH}_1 = P_1 \cdot S \rightarrow P_2 \cdot R \bullet P_2 \cdot S \rightarrow P_1 \cdot R \\ \text{CH}_2 = P_1 \cdot S \rightarrow P_3 \cdot R \bullet P_3 \cdot S \rightarrow P_1 \cdot R \\ \text{CH}_3 = P_1 \rightarrow \phi \parallel P_2 \rightarrow \phi \parallel P_3 \rightarrow \phi \end{array}$$

- let $CH_4 = P_1 \oplus \{CH_1, CH_2\}$
- a_1 is $P_1.S \rightarrow P_2.R$
- a_2 is $P_1.S \rightarrow P_3.R$
- Required to prove: $CH \models A[AG(P_1.S \implies AF(P_1.R)) \text{ W } CH_4.end]$

By Model Checking:

- $CH_1 \models AG(P_1.S \implies AF(P_1.R))$
- $CH_2 \models AG(P_1.S \implies AF(P_1.R))$

Conclude by branching correctness rule:

- $CH_4 \models AG(\hat{a}_1 \implies AG(P_1.S \implies AF(P_1.R))) \wedge AG(\hat{a}_2 \implies AG(P_1.S \implies AF(P_1.R)))$

By CTL deduction:

- $CH_4 \models AG(P_1.S \implies AF(P_1.R))$

Finally:

- $CH \models A[AG(P_1.S \implies AF(P_1.R)) \text{ W } CH_4.end]$

Similarly:

- $\llbracket CH \rrbracket \models A[AG(P_1.S \implies AF(P_1.R)) \text{ W } \hat{a}_4]$ where \hat{a}_4 signals end of CH_4

Chapter 8

Buyer-Seller Example

We illustrate our approach with the Buyer-Seller example from [1]. The following description is from [1]:

Consider a system consisting of four components: Buyer 1 (B_1), Buyer 2 (B_2), Seller (S) and Bank (Bk). Buyer 1 sends a book title to the Seller, who replies to both buyers by quoting a price for the given book. Depending on the price, Buyer 1 may try to haggle with Seller for a lower price, in which case Seller may either accept the new price or call off the transaction entirely. At this point, Buyer 2 takes Seller's response and coordinates with Buyer 1 to determine how much each should pay. In case Seller chose to abort, Buyer 2 would also abort. Otherwise, it would keep negotiating with Buyer 1 to determine how much it should pay. Buyer 1, having a limited budget, consults with the bank before replying to Buyer 2. Once Buyer 2 deems the amount to be satisfactory, he will ask the bank to pay the seller the agreed upon amount (Buyer 1 would be doing the same thing *in parallel*).

8.1 The Choreography

Listing 8.1 depicts global choreography from the Buyer-Seller example in [1].

8.2 Input File

```
-- This is a comment. A comment must start with 2 dashes "--"  
-- and be at the start of a line  
-- Each choreography is written on a single line.  
-- The first choreography written will be the global choreography  
-- The first event in the global choreography is the start state  
-- Sequential events are delimited by '*'
```

Listing 8.1: Global choreography of the Buyer/Seller example

```

CH = B1.S→S.R • S.S → {B1.R, B2.R} • B1 ⊕ {CH1, ε} • CH2 •
  CH7
CH1 = B1.S→S.R • S.S → {B1.R, B2.R}
CH2 = B2 ⊕ {CH3, ε}
CH3 = while(B2.C) {
    B1.C→ Bk.InfR • Bk.InfS → B1.R • B1.C→ B2.R
  } • CH4
CH4 = CH5 || CH6
CH5 = B2.MS→Bk.MR2 • Bk.MS2 → S.R
CH6 = B1.MS→Bk.MR1 • Bk.MS1 → S.R
CH7 = B1.E → φ || B2.E → φ || Bk.E → φ || S.E → φ

```

```

CH = B1.S > S.R * S.S > {B1.R, B2.R} * B1 + {CH1, E} * CH2 * CH7
-- Send/Recv messages can have one or multiple receivers
CH1 = B1.S > S.R * S.S > {B1.R, B2.R}
-- 'E' stands for epsilon, where we "skip" the branching step
CH2 = B2 + {CH3, E}
-- Inside while loops, '^' is used instead of '*' for sequential events
-- Nested loops are written in their own choreography
-- To use branching inside loops, the branching must be written
-- in its own choreography
CH3 = while(B2.C) {B1.C > Bk.InfR ^ Bk.InfS > B1.R ^ B1.C > B2.R} * CH4
-- Parallel choreographies are delimited by "||"
-- Parallel choreographies are written in their own choreography
-- Parallel choreographies contain sequential events only
CH4 = CH5 || CH6
CH5 = B2.MS > Bk.MR2 * Bk.MS2 > S.R
CH6 = B1.MS > Bk.MR1 * Bk.MS1 > S.R
CH7 = CH8 || CH9 || CH10 || CH11
-- END represents the termination of a process
CH8 = B1.E > END
CH9 = B2.E > END
CH10 = Bk.E > END
CH11 = S.E > END

```

8.3 Kripke Structure Generation

The following is the Kripke structure generated from the Buyer-Seller choreography. This was automatically generated by our implementation.

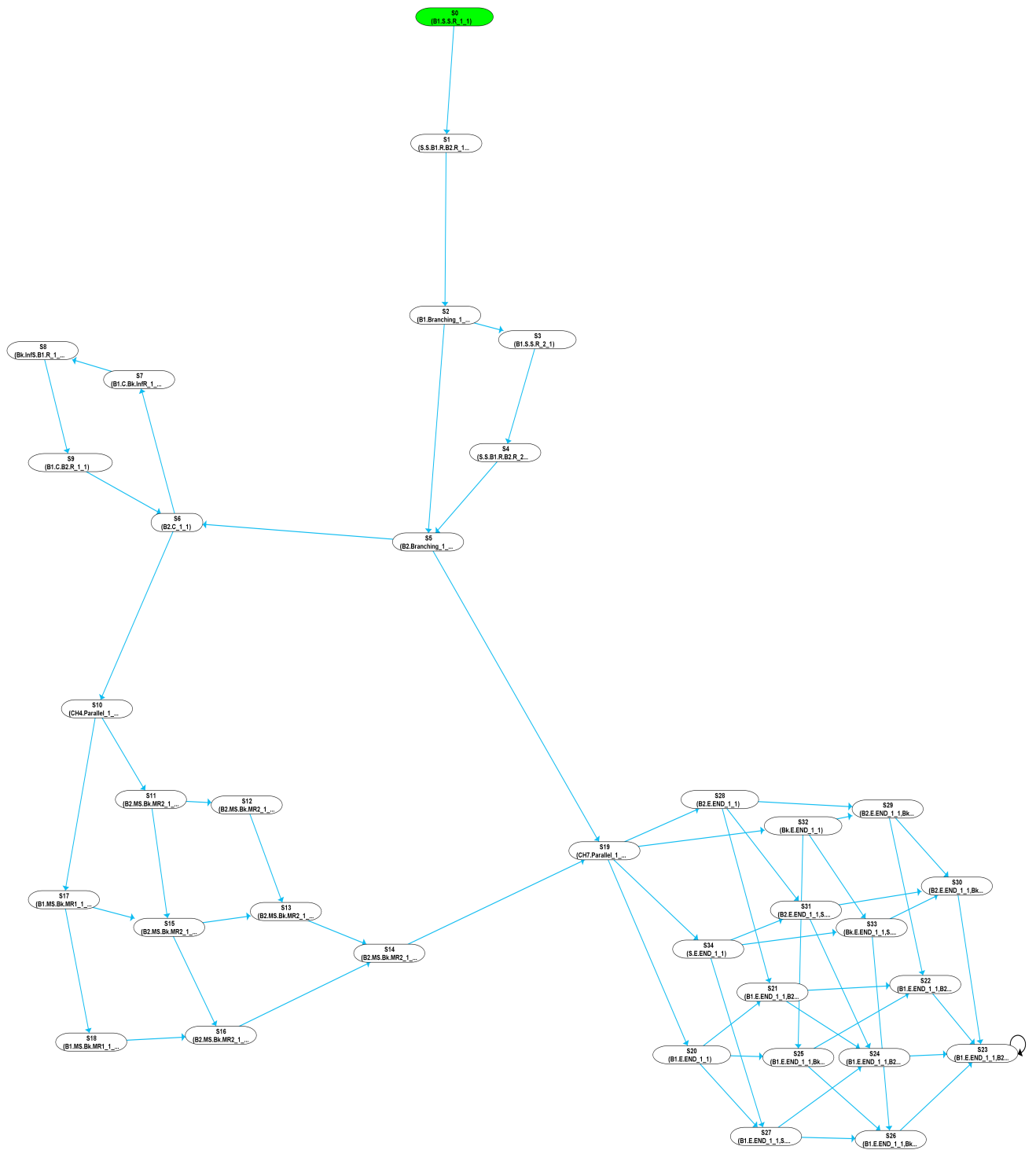


Figure 8.1: Automatically Generated Buyer-Seller Example

8.4 Label Definitions

We have 4 components:

- Buyer 1
- Buyer 2
- Seller
- Banker

The following shows the atomic propositions (comma separated) each state contains. The atomic propositions were auto generated by *name_i_1* where *name* is the concatenation of the elements of an event dot separated, *i* is the counter so that the proposition is unique in case repetition of name, and 1 is to indicate that the atomic proposition belongs to process 1 (the global choreography).

states:

```
S0:B1.S.S.R_1_1
S1:S.S.B1.R.B2.R_1_1
S2:B1.Branching_1_1
S3:B1.S.S.R_2_1
S4:S.S.B1.R.B2.R_2_1
S5:B2.Branching_1_1
S6:B2.C_1_1
S7:B1.C.Bk.InfR_1_1
S8:Bk.InfS.B1.R_1_1
S9:B1.C.B2.R_1_1
S10:CH4.Parallel_1_1
S11:B2.MS.Bk.MR2_1_1
S12:B2.MS.Bk.MR2_1_1,Bk.MS2.S.R_1_1
S13:B2.MS.Bk.MR2_1_1,Bk.MS2.S.R_1_1,B1.MS.Bk.MR1_1_1
S14:B2.MS.Bk.MR2_1_1,Bk.MS2.S.R_1_1,B1.MS.Bk.MR1_1_1,Bk.MS1.S.R_1_1
S15:B2.MS.Bk.MR2_1_1,B1.MS.Bk.MR1_1_1
S16:B2.MS.Bk.MR2_1_1,B1.MS.Bk.MR1_1_1,Bk.MS1.S.R_1_1
S17:B1.MS.Bk.MR1_1_1
S18:B1.MS.Bk.MR1_1_1,Bk.MS1.S.R_1_1
S19:CH7.Parallel_1_1
S20:B1.E.END_1_1
S21:B1.E.END_1_1,B2.E.END_1_1
S22:B1.E.END_1_1,B2.E.END_1_1,Bk.E.END_1_1
S23:B1.E.END_1_1,B2.E.END_1_1,Bk.E.END_1_1,S.E.END_1_1
S24:B1.E.END_1_1,B2.E.END_1_1,S.E.END_1_1
S25:B1.E.END_1_1,Bk.E.END_1_1
```

S26: B1.E.END_1_1, Bk.E.END_1_1, S.E.END_1_1
 S27: B1.E.END_1_1, S.E.END_1_1
 S28: B2.E.END_1_1
 S29: B2.E.END_1_1, Bk.E.END_1_1
 S30: B2.E.END_1_1, Bk.E.END_1_1, S.E.END_1_1
 S31: B2.E.END_1_1, S.E.END_1_1
 S32: Bk.E.END_1_1
 S33: Bk.E.END_1_1, S.E.END_1_1
 S34: S.E.END_1_1

We describe each atomic proposition:

B1.S.S.R_1_1 : Buyer 1 asks seller for price of an item

S.S.B1.R.B2.R_1_1 : Seller replies to both buyers quoting the price

B1.Branching_1_1 : Branching point to for buyer 1 to either haggle or continue

B1.S.S.R_2_1 : Buyer 1 haggles with seller for price

S.S.B1.R.B2.R_2_1 : Seller replies to both buyers either accepting buyer 1's haggle or aborting transaction

B2.Branching_1_1 : Branching point to either continue transaction or abort

B2.C_1_1 : While loop condition for Buyer 2's satisfaction of price

B1.C.Bk.InfR_1_1 : Buyer 1 consults Banker

Bk.InfS.B1.R_1_1 : Banker replies to Buyer 1

B1.C.B2.R_1_1 : Buyer 1 negotiates with Buyer 2 on the price

CH4.Parallel_1_1 : Parallel point for Buyers' payment

B2.MS.Bk.MR2_1_1 v Buyer 2 asks banker to wire the seller the agreed amount

Bk.MS2.S.R_1_1 : Banker wires Buyer 2's money to the seller

B1.MS.Bk.MR1_1_1 : Buyer 1 asks banker to wire the seller the agreed amount

Bk.MS1.S.R_1_1 : Banker wires Buyer 1's money to the seller

CH7.Parallel_1_1 : Parallel point for components' termination

B1.E.END_1_1 : Buyer 1 reaches termination

B2.E.END_1_1 : Buyer 2 reaches termination

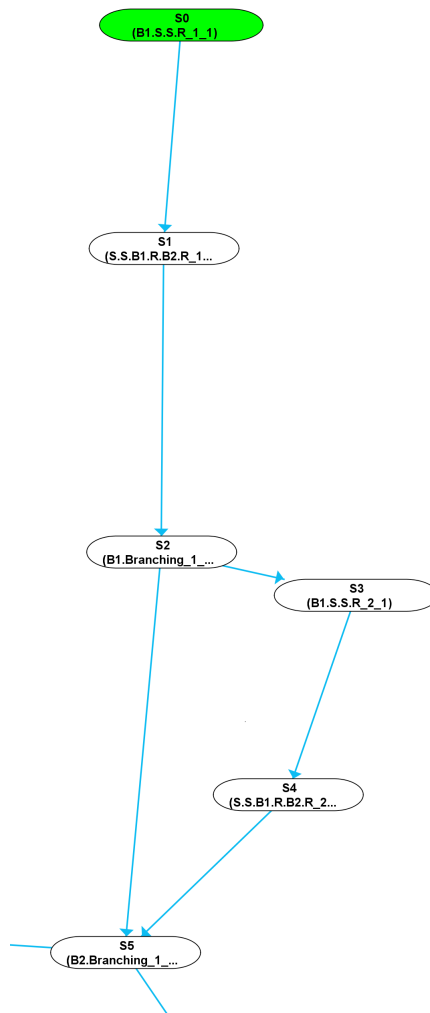
Bk.E.END_1_1 : Banker reaches termination

S.E.END_1_1 : Seller reaches termination

8.5 Explanation of the Kripke Structure

We explain below the various segments of the Kripke structure of the Buyer-Seller example.

8.5.1 Buyer 1 Hagggle



S0: Buyer 1 asks Seller for price

S1: Seller replies to both Buyer 1 and Buyer 2

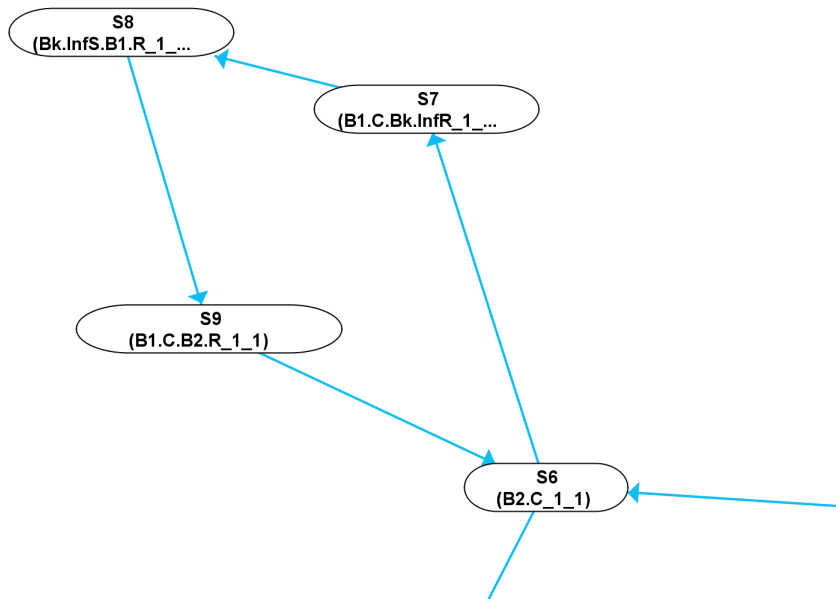
S2: Branching choice, Buyer will either decide to haggle or not

S3: Buyer 1 is haggling

S4: Seller replies to both buyers with either agreement to the haggle or calling off the transaction

S5: Branching choice, either we continue with the transaction or abort if Buyer 1 had haggled unsuccessfully

8.5.2 Banker Consulting

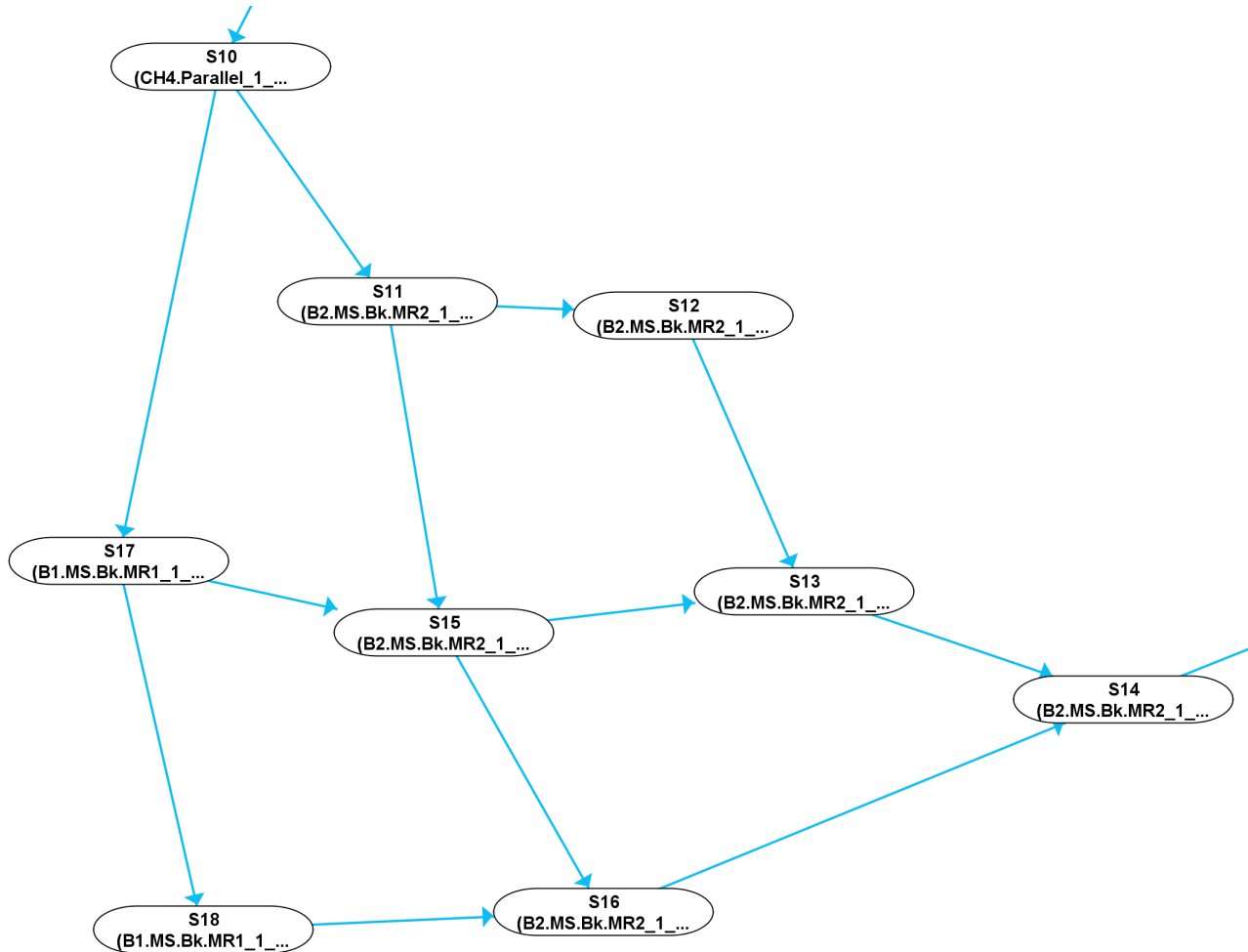


S6: Either Buyer 1 decided not to haggle ($S2 \rightarrow S5$) or haggled successfully ($S2 \rightarrow S3 \rightarrow S4 \rightarrow S5$)

S7, S8: Banker is consulting with Buyer 1

S9: Buyer 1 and Buyer 2 negotiate on price. Once Buyer 2 is satisfied with price, we exit the loop going from $S6 \rightarrow S10$

8.5.3 Buyers Authorize Payment in Parallel



All the routes from S10 to S14 are the different ways for $CH4 : CH5 \parallel CH6$ to happen

Case 1: $S10 \rightarrow S11 \rightarrow S12 \rightarrow S13 \rightarrow S14$, this simply $CH5 \bullet CH6$.

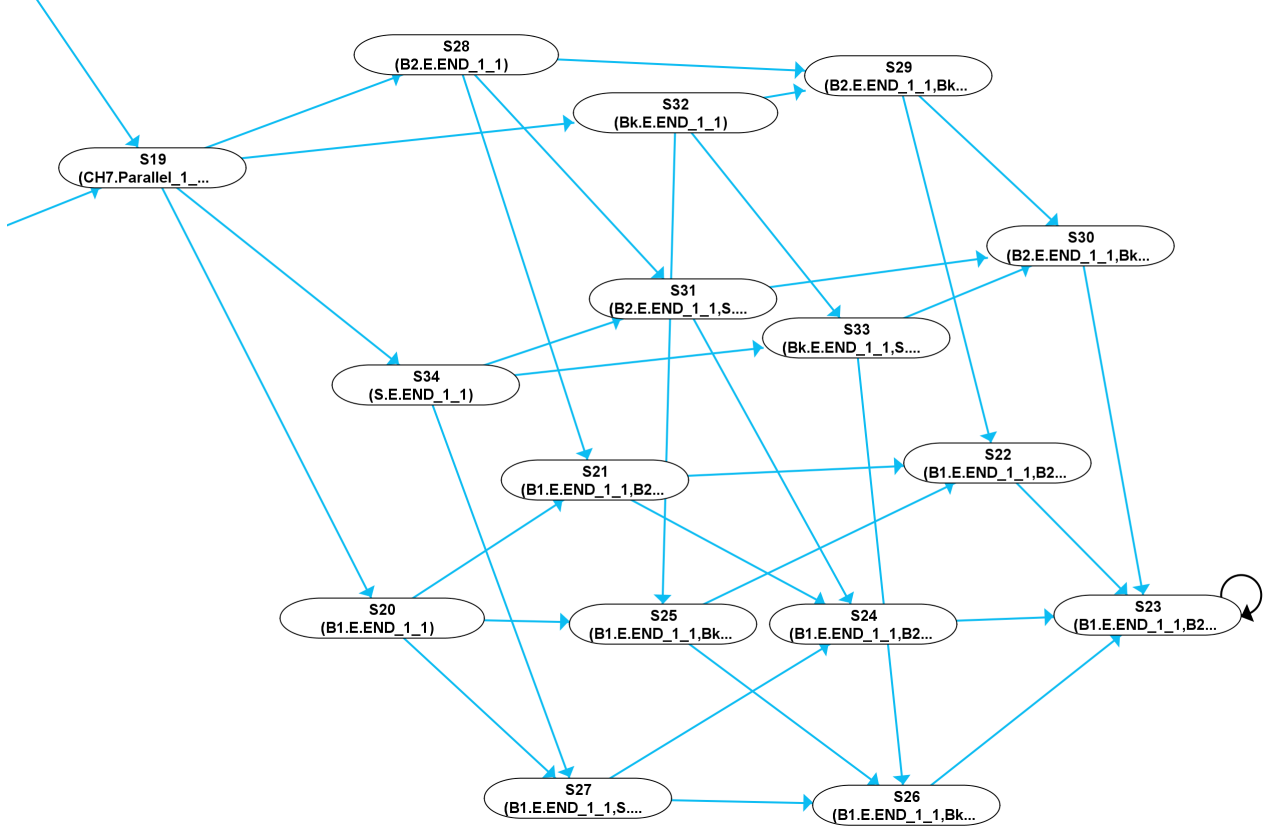
Case 2: $S10 \rightarrow S17 \rightarrow S18 \rightarrow S16 \rightarrow S14$, this simply $CH6 \bullet CH5$.

Case 3: Reaching S15 ($S10 \rightarrow S17 \rightarrow S15$ or $S10 \rightarrow S11 \rightarrow S15$) Both Buyers have asked for the money to wired (regardless of who asked first)

Case 3.1: $S15 \rightarrow S13 \rightarrow S14$ Banker decides to wire Buyer 2's money then Buyer 1's

Case 3.2: $S15 \rightarrow S16 \rightarrow S14$ Banker decides to wire Buyer 1's money then Buyer 2's

8.5.4 System Termination



$S19 \rightarrow S23$ are all the different ways for $CH7$ to happen

8.6 Properties Model Checked

Here are some of the properties model we checked in Eshmun for the above Kripke structure. They include the properties tested on the implemented system in [1] and some of our own.

1. **AlwaysTerminate:** for all paths, we will terminate.

$$AF(B1.E.END_1_1 \ \& \ B2.E.END_1_1 \ \& \ Bk.E.END_1_1 \ \& \ S.E.END_1_1)$$

2. **AuthorizePayment:** no payment unless buyer requests.

$$(AG(A(!Bk.MS2.S.R_1_1 \ W \ B2.MS.Bk.MR2_1_1))) \ \& \\ (AG(A(!Bk.MS1.S.R_1_1 \ W \ B1.MS.Bk.MR1_1_1)))$$

3. **EndPorts:** if a process reaches the end port, other processes will also reach their end ports.

$$\text{AG}((B1.E.END_1_1 \mid B2.E.END_1_1 \mid Bk.E.END_1_1 \mid S.E.END_1_1) \Rightarrow (\text{AF}(B1.E.END_1_1 \ \& \ B2.E.END_1_1 \ \& \ Bk.E.END_1_1 \ \& \ S.E.END_1_1)))$$

4. **NoLivelock:** system doesn't suffer from livelock.

$$\text{AF}(\text{AG}(!B2.C_1_1))$$

5. **LoopOrTerminate:** either we follow a path that leads to a loop, or we terminate.

$$\text{AG}(!B2.C_1_1 \Rightarrow ((\text{EF}(B2.C_1_1) \mid (\text{AF}(B1.E.END_1_1 \ \& \ B2.E.END_1_1 \ \& \ Bk.E.END_1_1 \ \& \ S.E.END_1_1))))))$$

6. **NoLivelockAfterLoop:** after exiting a loop, the system doesn't enter livelock.

$$\text{AG}(B2.C_1_1 \Rightarrow (\text{AX}(!B1.C.Bk.InfR_1_1 \Rightarrow (\text{AG}(!B2.C_1_1))))))$$

7. **PayOnce:** buyer only pays once.

$$(\text{AG}(Bk.MS1.S.R_1_1 \Rightarrow (\text{AF}(!Bk.MS1.S.R_1_1 \Rightarrow (\text{AG}(!Bk.MS1.S.R_1_1)))))) \ \& \ (\text{AG}(Bk.MS2.S.R_1_1 \Rightarrow (\text{AF}(!Bk.MS2.S.R_1_1 \Rightarrow (\text{AG}(!Bk.MS2.S.R_1_1))))))$$

8. **TermAfterExit:** after exiting the loop we will terminate.

$$\text{AG}(B2.C_1_1 \Rightarrow (\text{AX}(!B1.C.Bk.InfR_1_1 \Rightarrow (\text{AF}(B1.E.END_1_1 \ \& \ B2.E.END_1_1 \ \& \ Bk.E.END_1_1 \ \& \ S.E.END_1_1))))))$$

All of the above properties were model checked true, except for **AlwaysTerminate** and **NoLivelock**, which were modified to **NoLiveLockAfterLoop** and **LoopOrTerminate** respectively to better fit the scope of the example.

Chapter 9

Conclusions and Future Work

We were able to devise an operational semantics for choreographies, and used this to automatically generate the Kripke structure that gives the behavior of a choreography. We devised a method for verifying the correctness of choreographies which avoids state explosion. We also came up with the semantics and correctness for implementation of choreographies. Finally, we devised theorems which state that if the global choreography satisfy certain formulae, then the implemented system does as well (for sequential correctness). This was done for a sub-logic of CTL. Future work includes:

1. Write correctness theory for all operations of the choreography
2. Undertake more case studies
3. Increase the set of CTL formulae that can be verified
4. Consider infinite-state choreographies, i.e., the state variables have infinite domains

Bibliography

- [1] R. A. Rayan Hallal, Mohamad Jaber, “From global choreography to efficient distributed implementation,” pp. 756–763, 2018.
- [2] E. A. Emerson and E. M. Clarke, “Using branching time temporal logic to synthesize synchronization skeletons,” *Science of Computer Programming*, vol. 2, pp. 241 – 266, 1982.
- [3] P. C. Attie, K. Dak-Al-Bab, and M. Sakr, “Model and program repair via sat solving,” *ACM Transactions on Embedded Systems*, pp. –, 2017.
- [4] A. Basu, S. Bensalem, M. Bozga, J. Combaz, M. Jaber, T. Nguyen, and J. Sifakis, “Rigorous component-based system design using the BIP framework,” *IEEE Software*, vol. 28, no. 3, pp. 41–48, 2011.

